

FIG. 1A

DCH/DCH, DCH/DCH+DSCH, DCH/DSCH+DSCH Ctrl

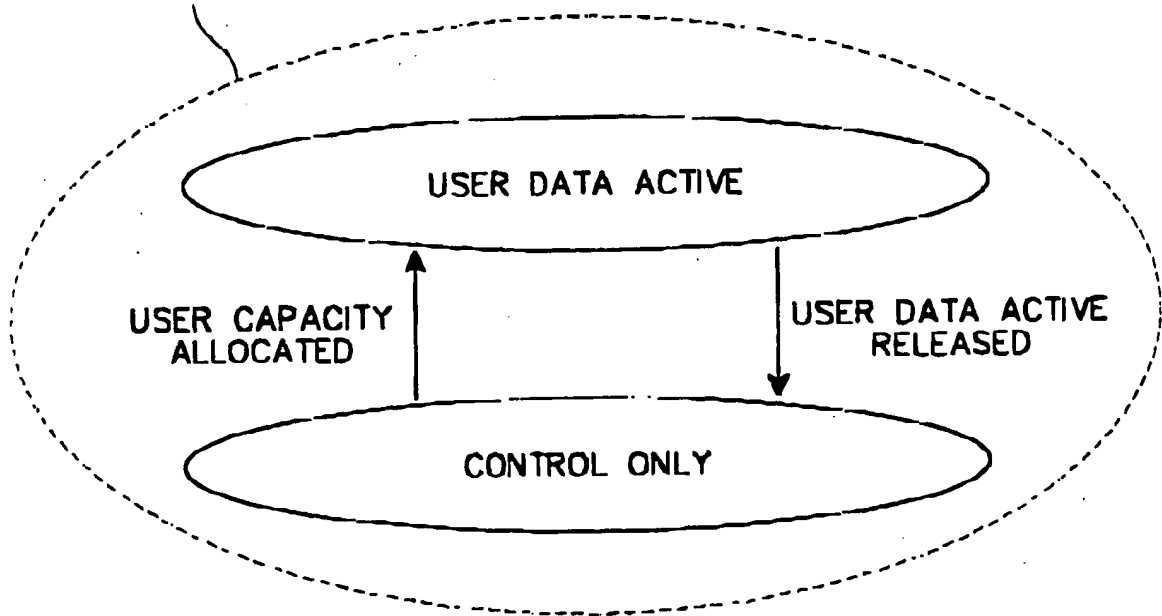


FIG. 1B

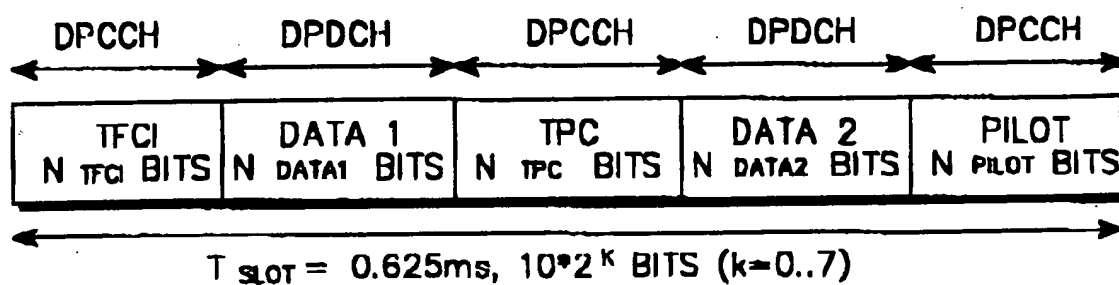


FIG. 2A

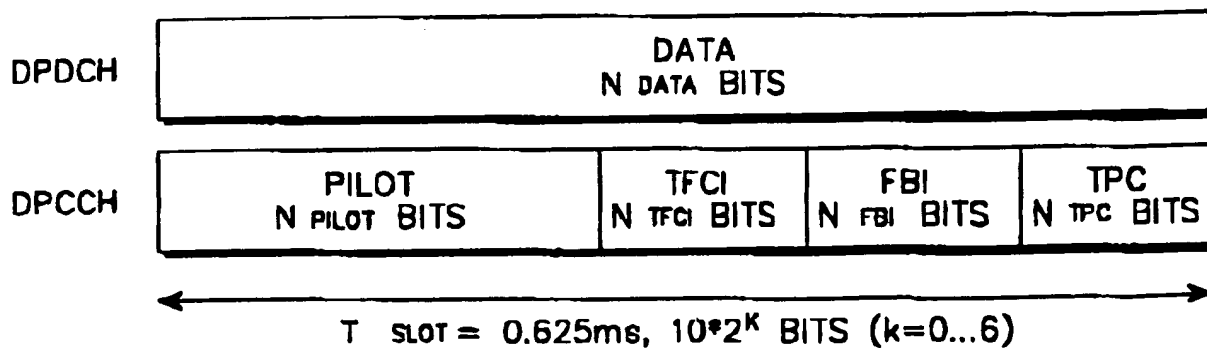


FIG. 2B



FIG. 3A

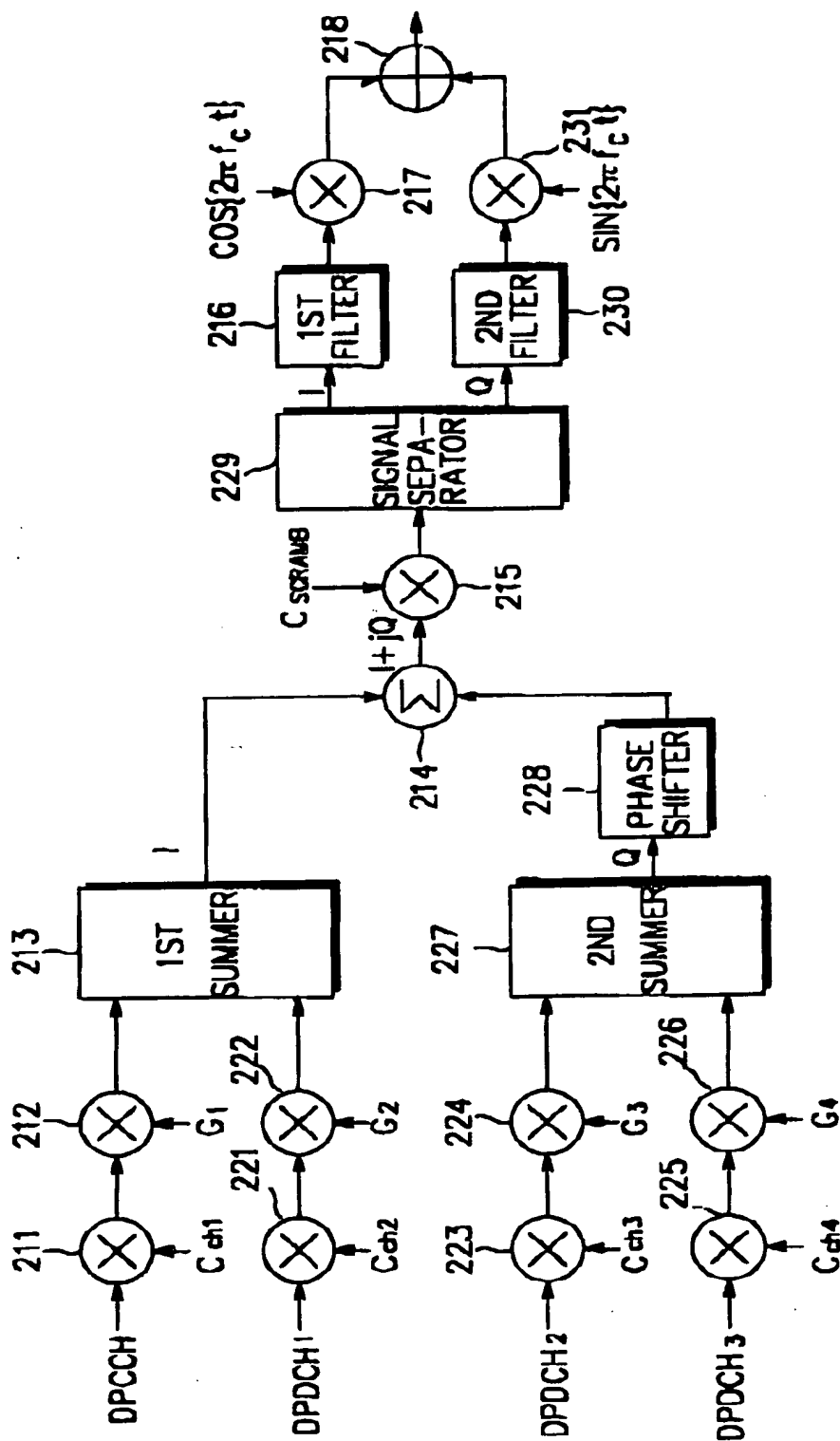
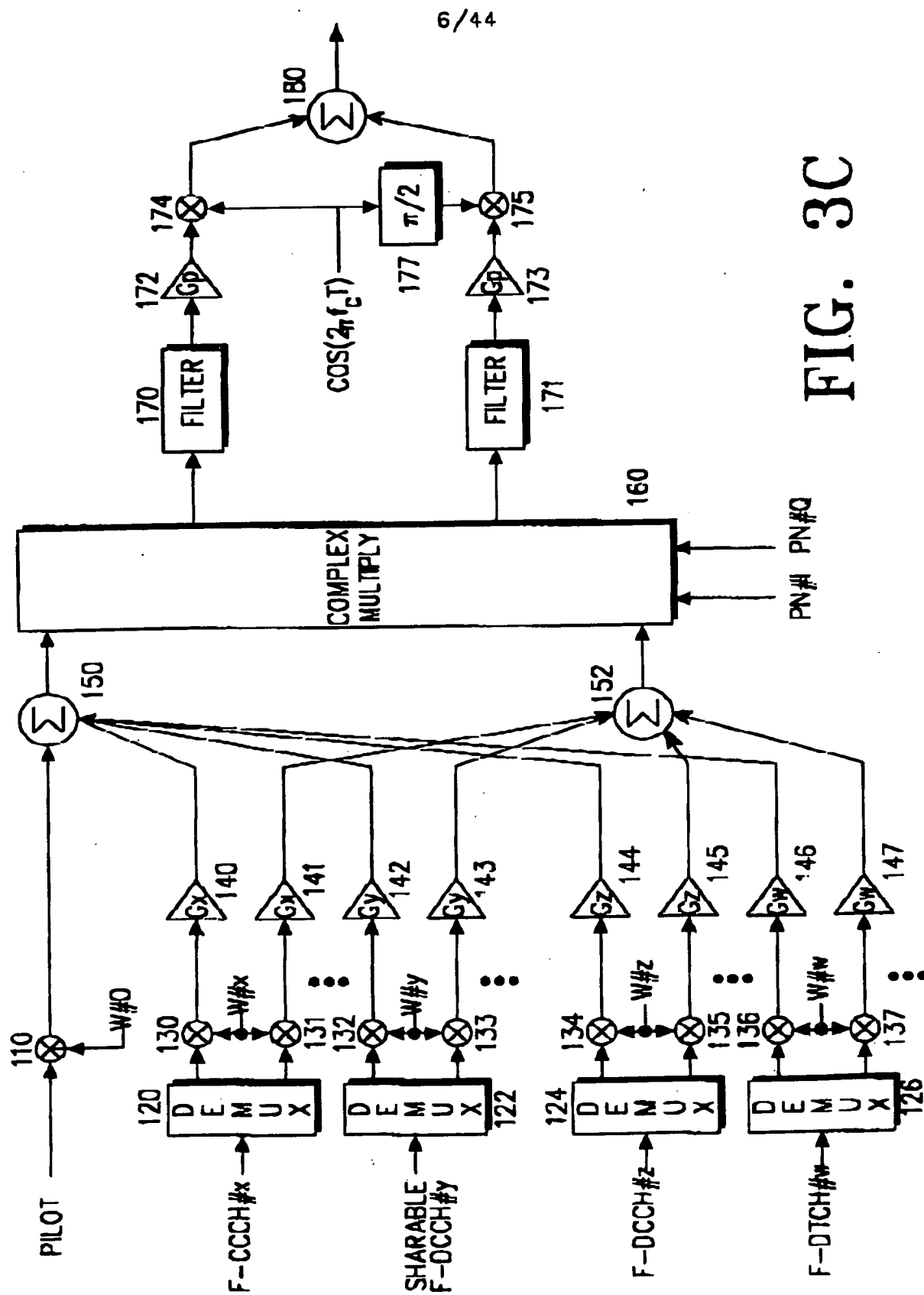


FIG. 3B



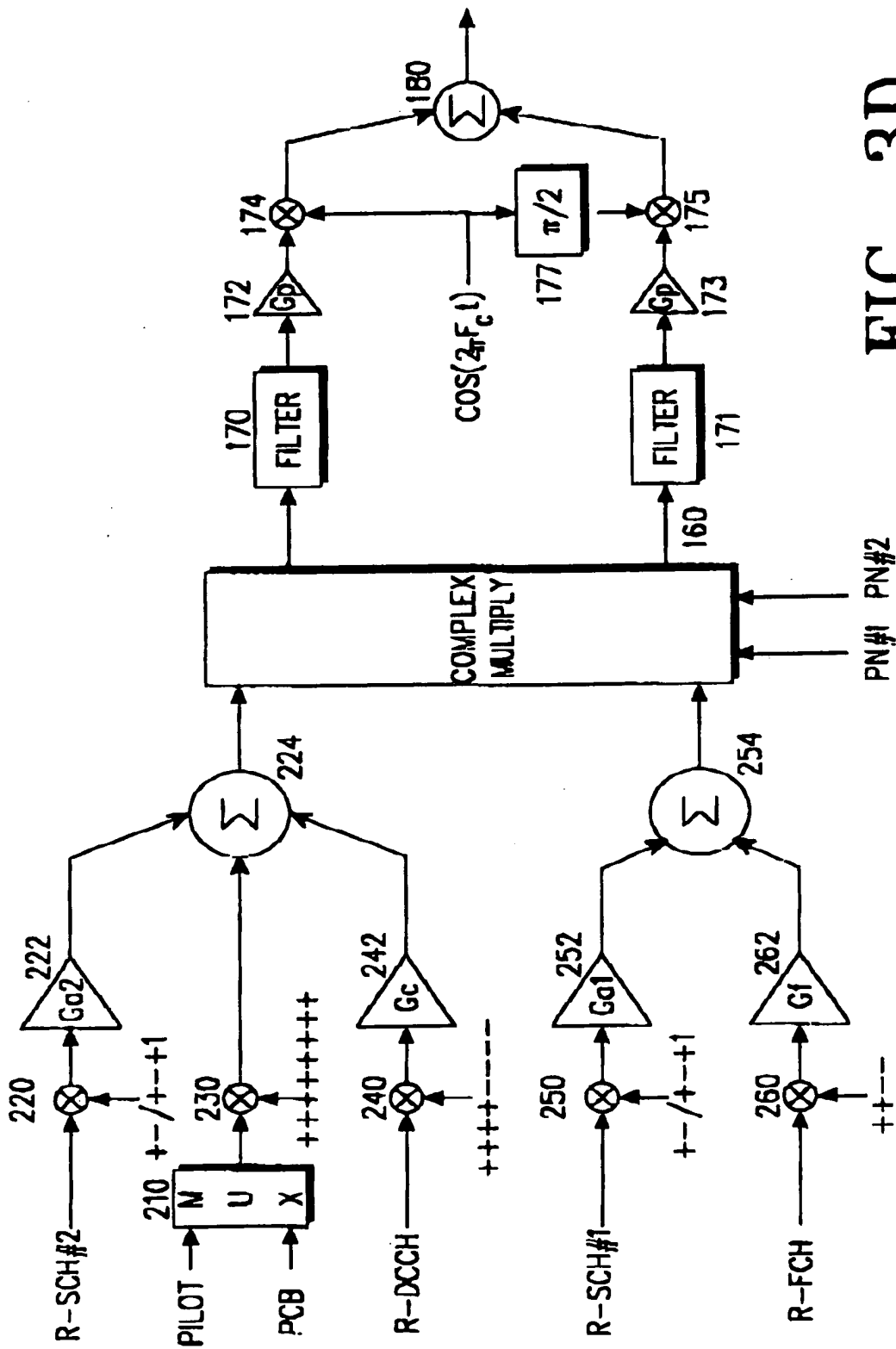
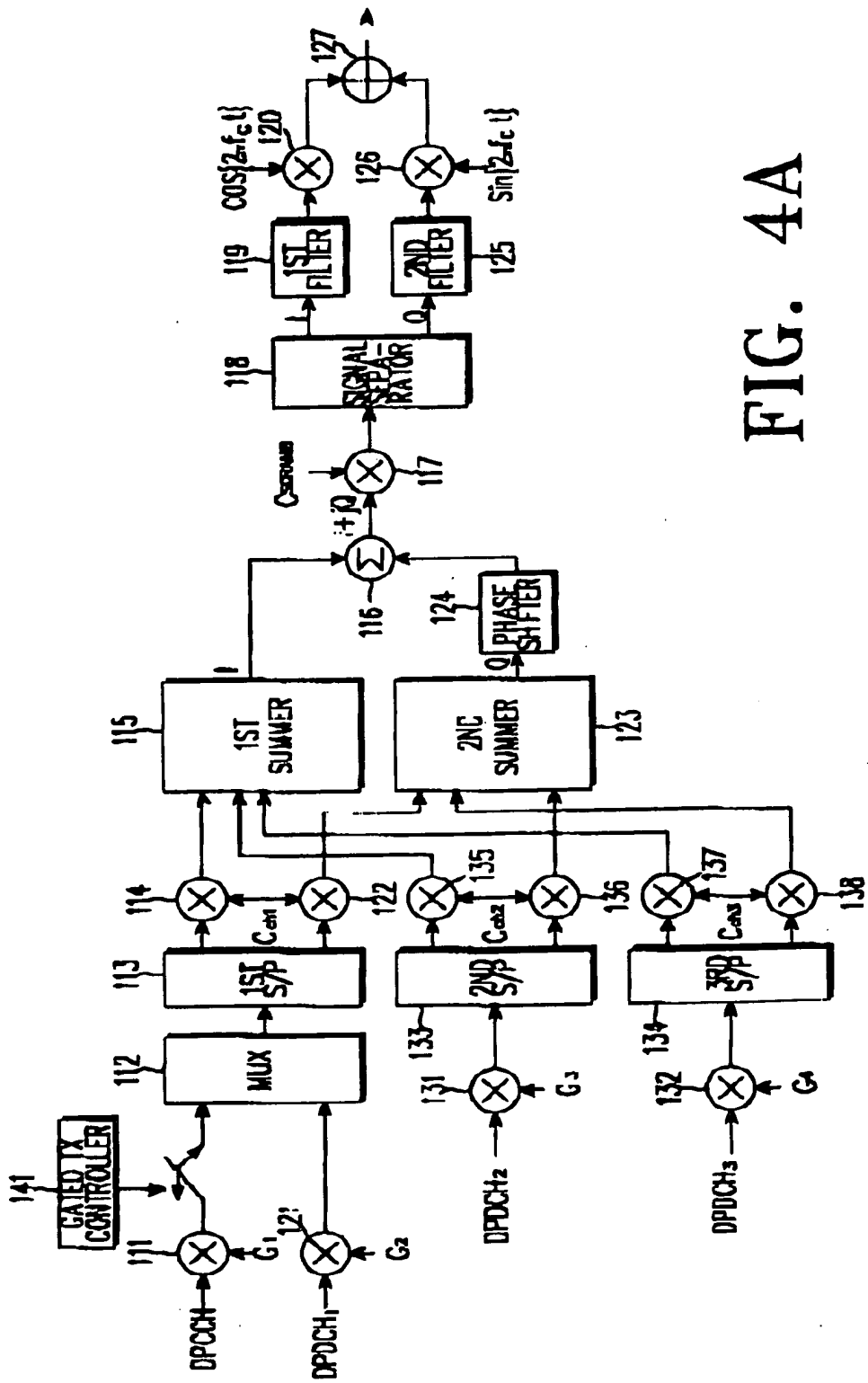


FIG. 3D



**FIG. 4A**



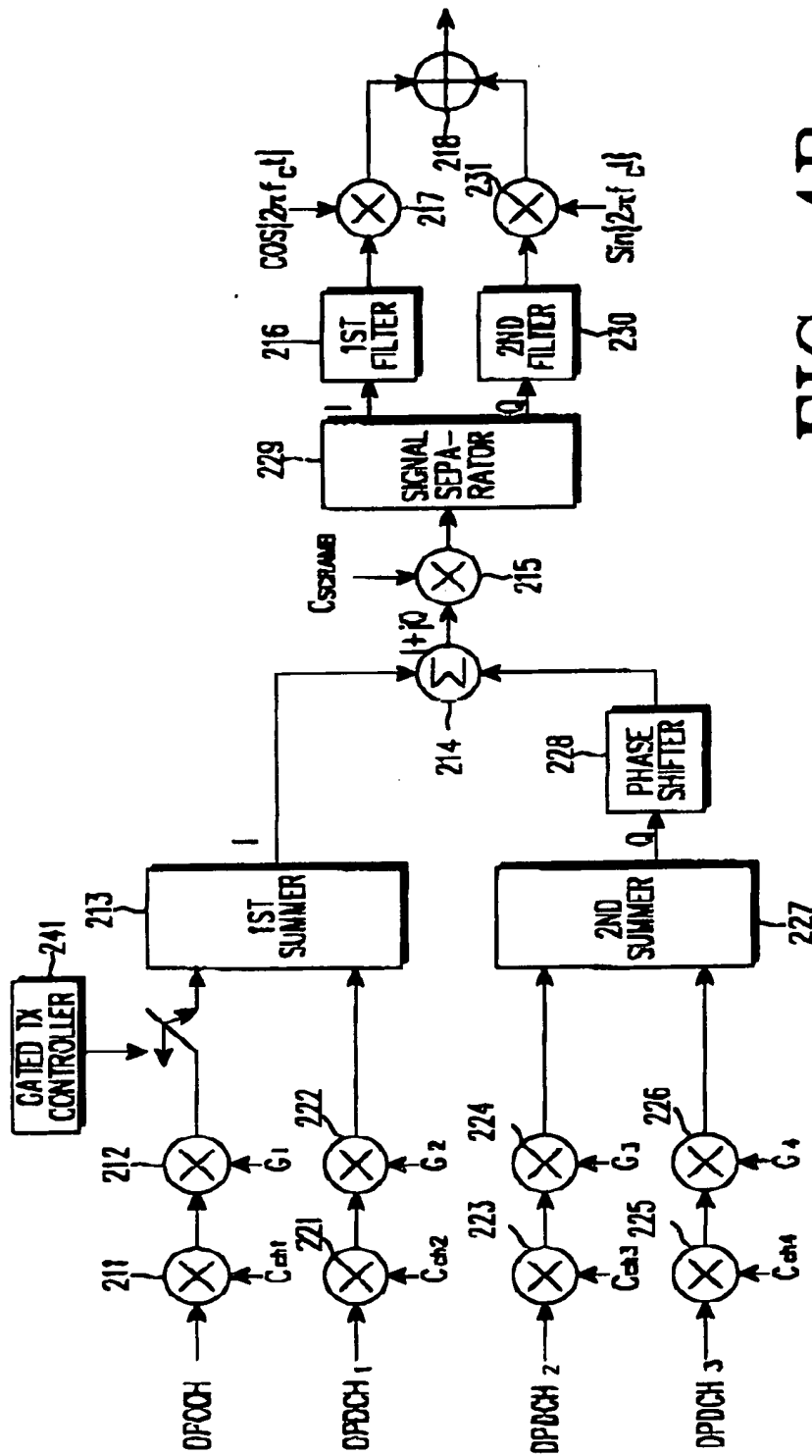


FIG. 4B

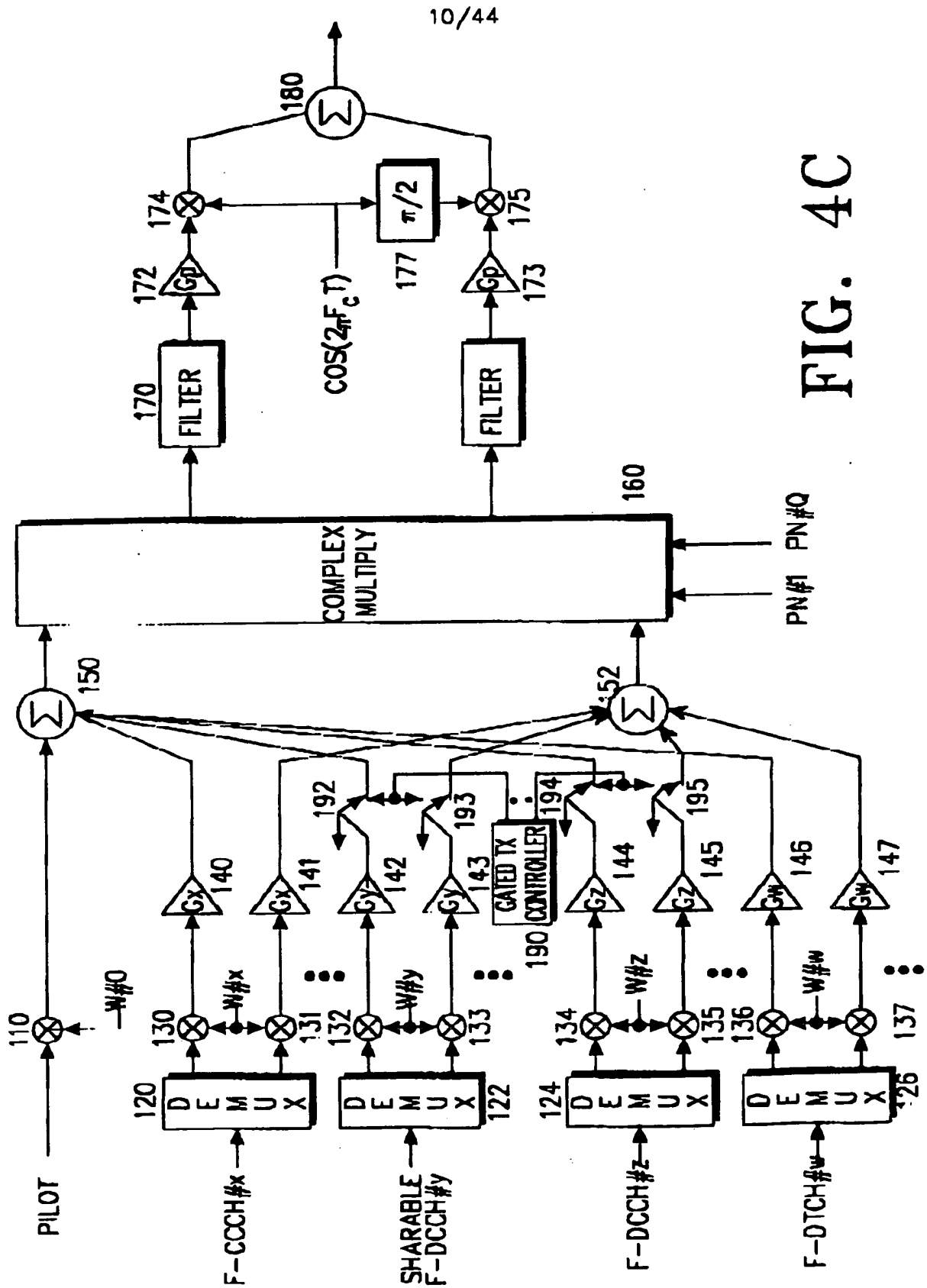


FIG. 4C

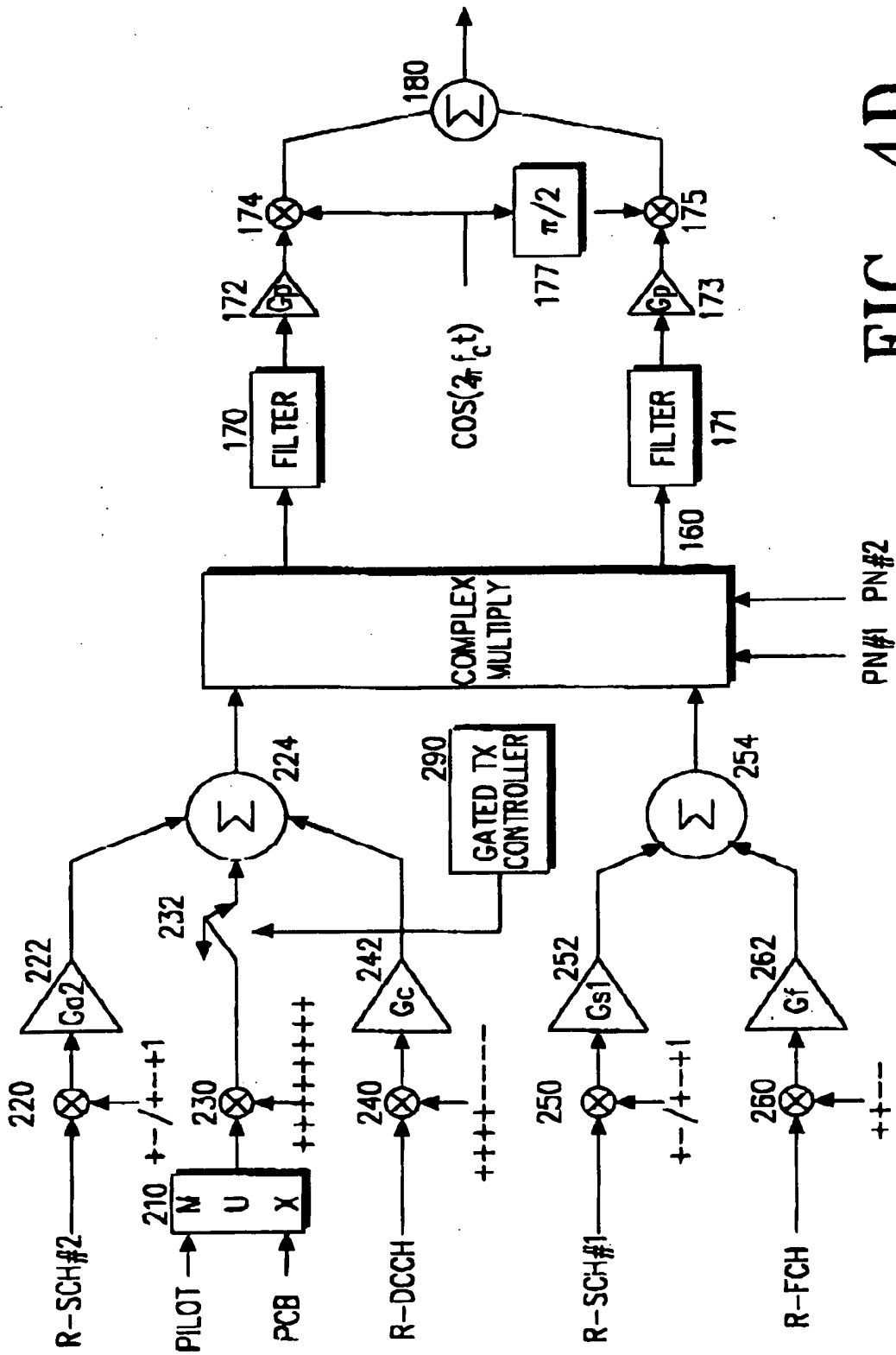


FIG. 4D



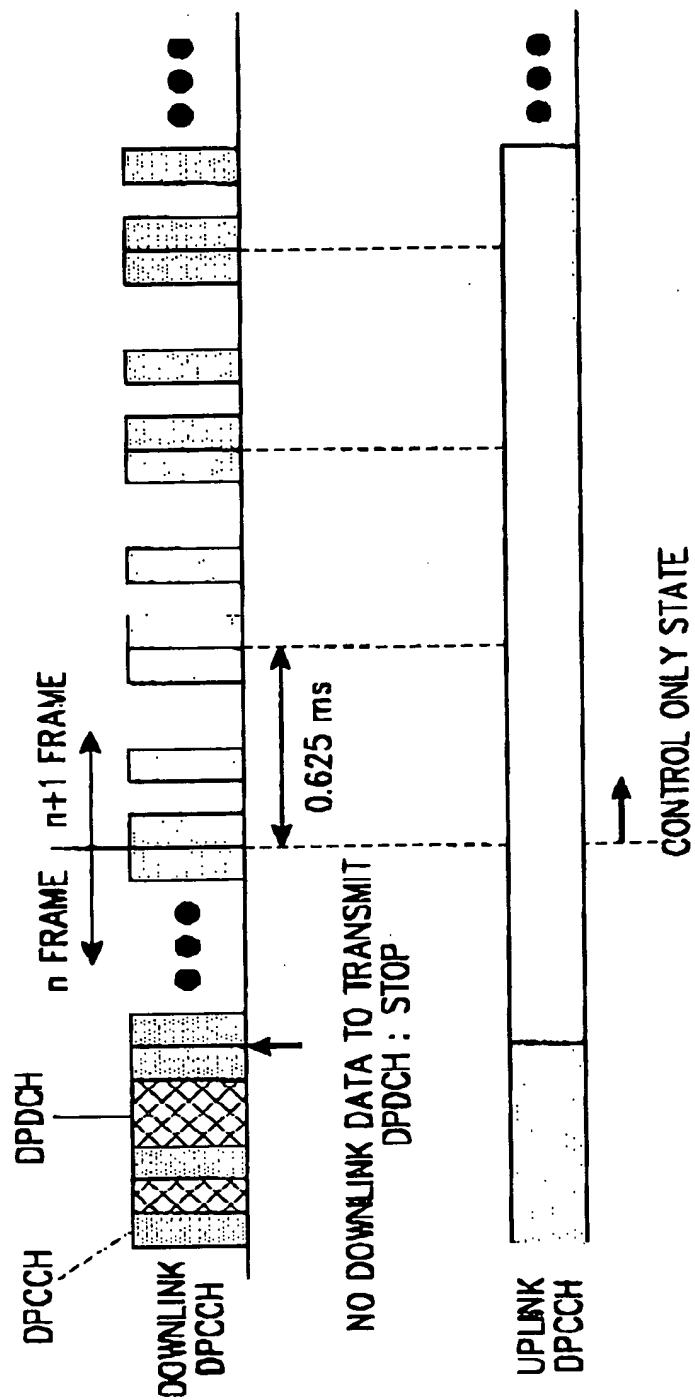


FIG. 5B

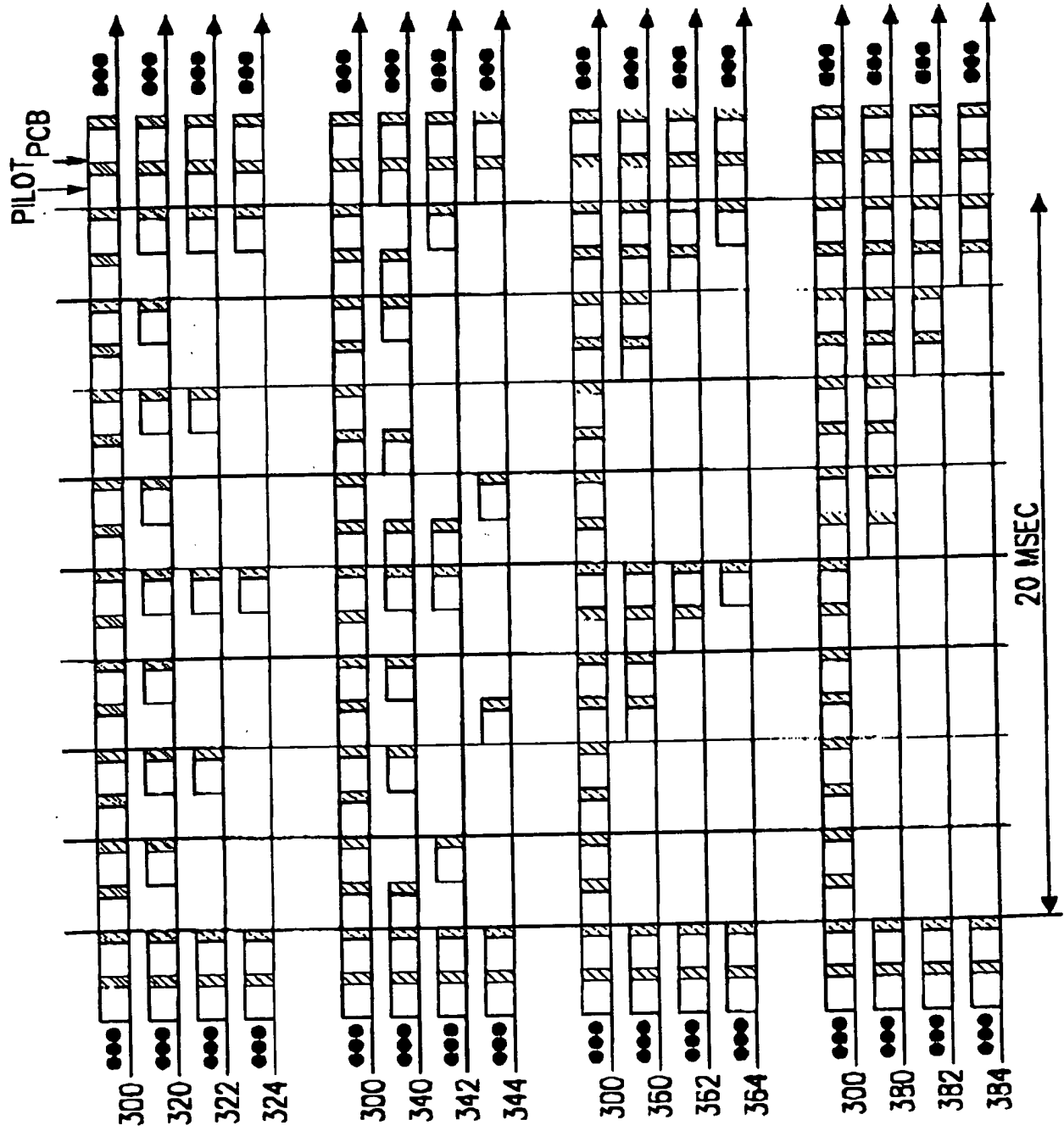


FIG. 5C

FIG. 6A

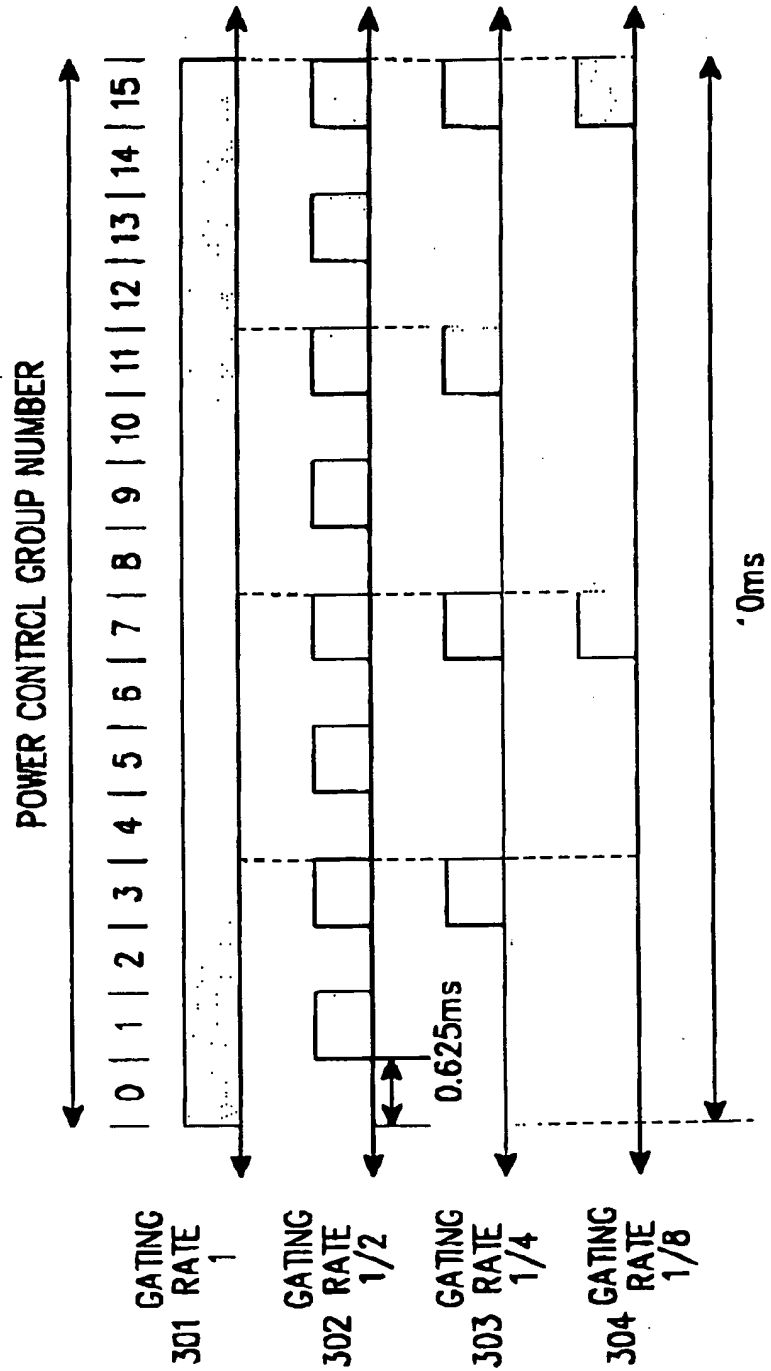


FIG. 6B

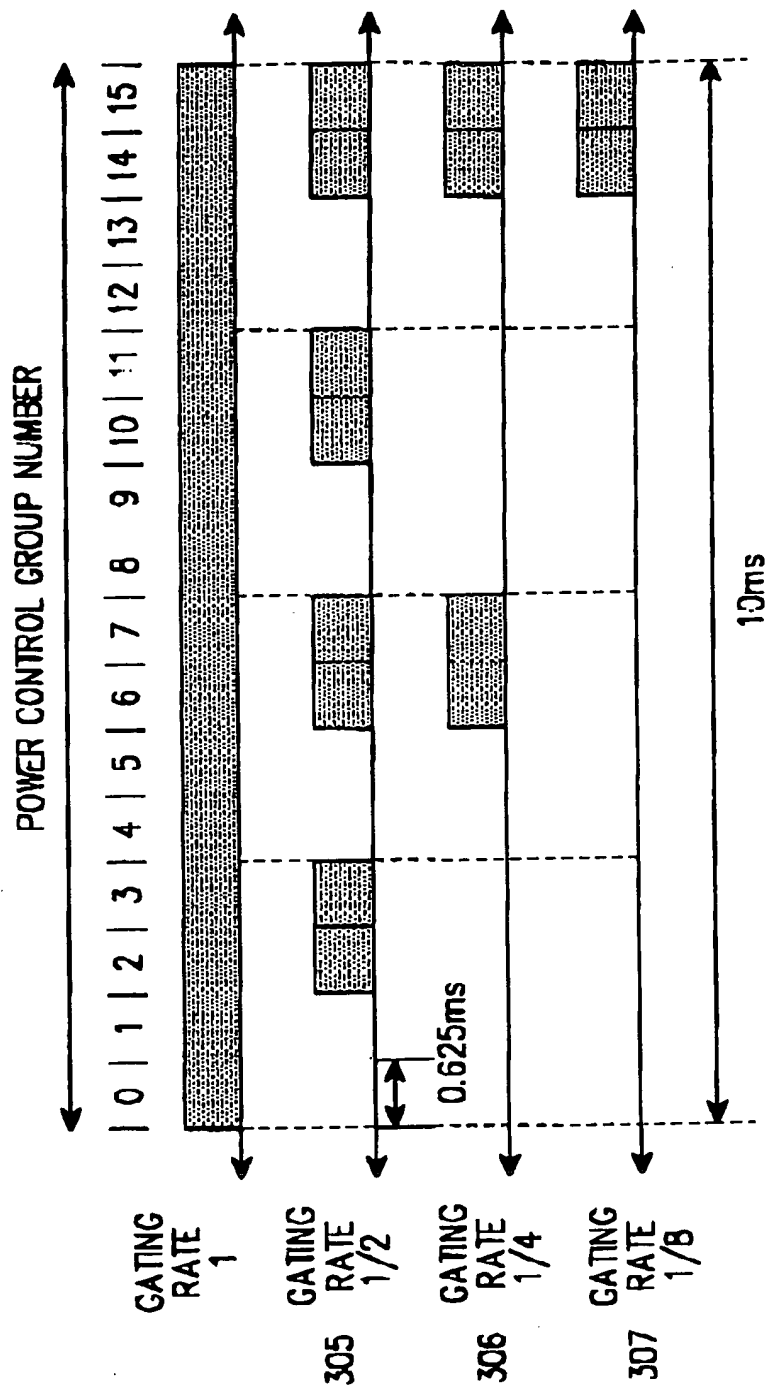




FIG. 7A

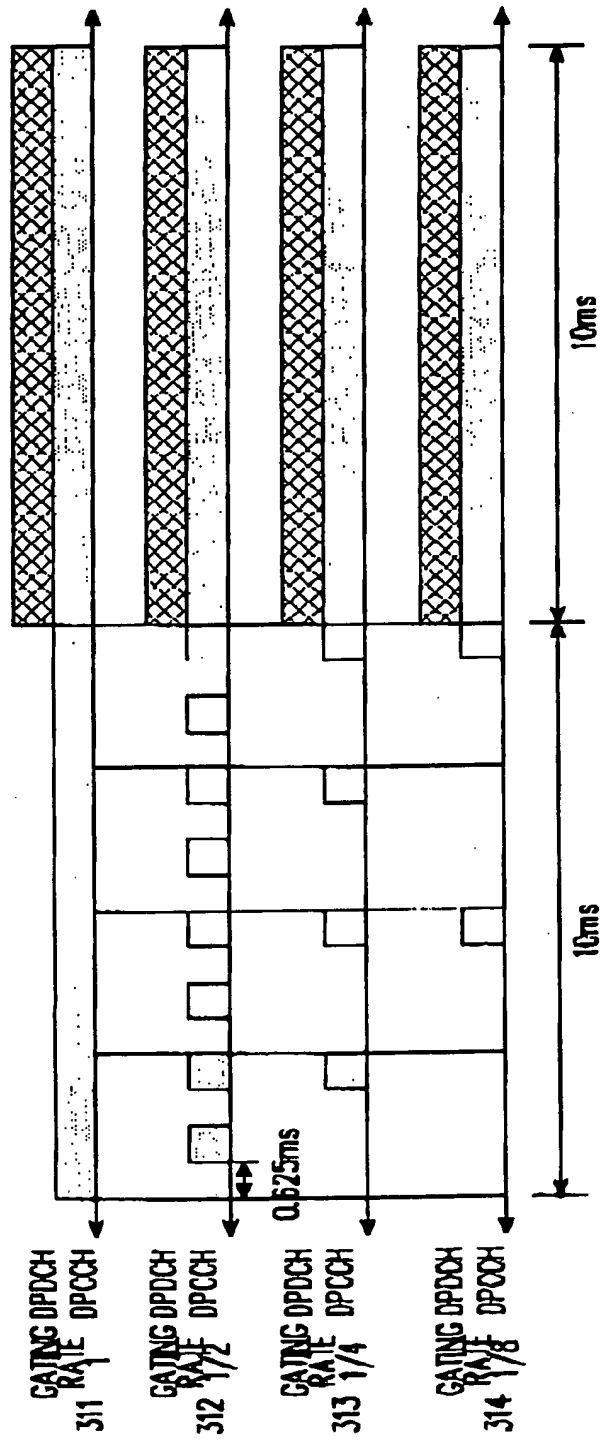


FIG. 7B

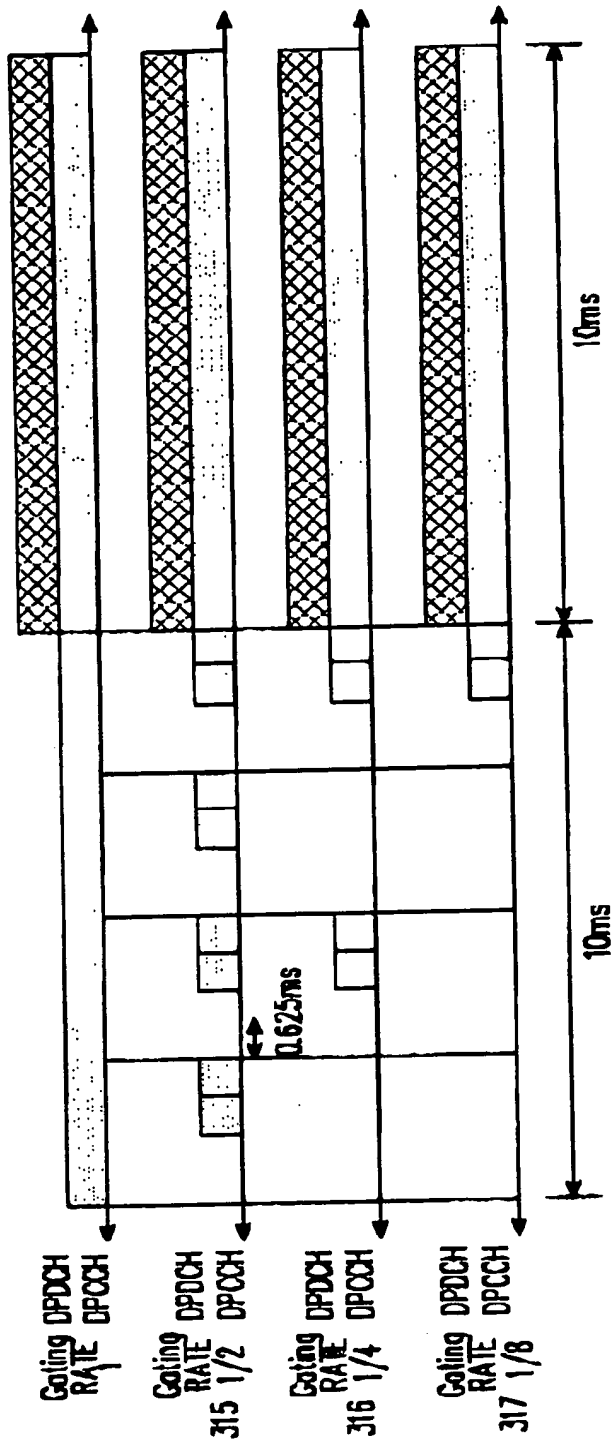
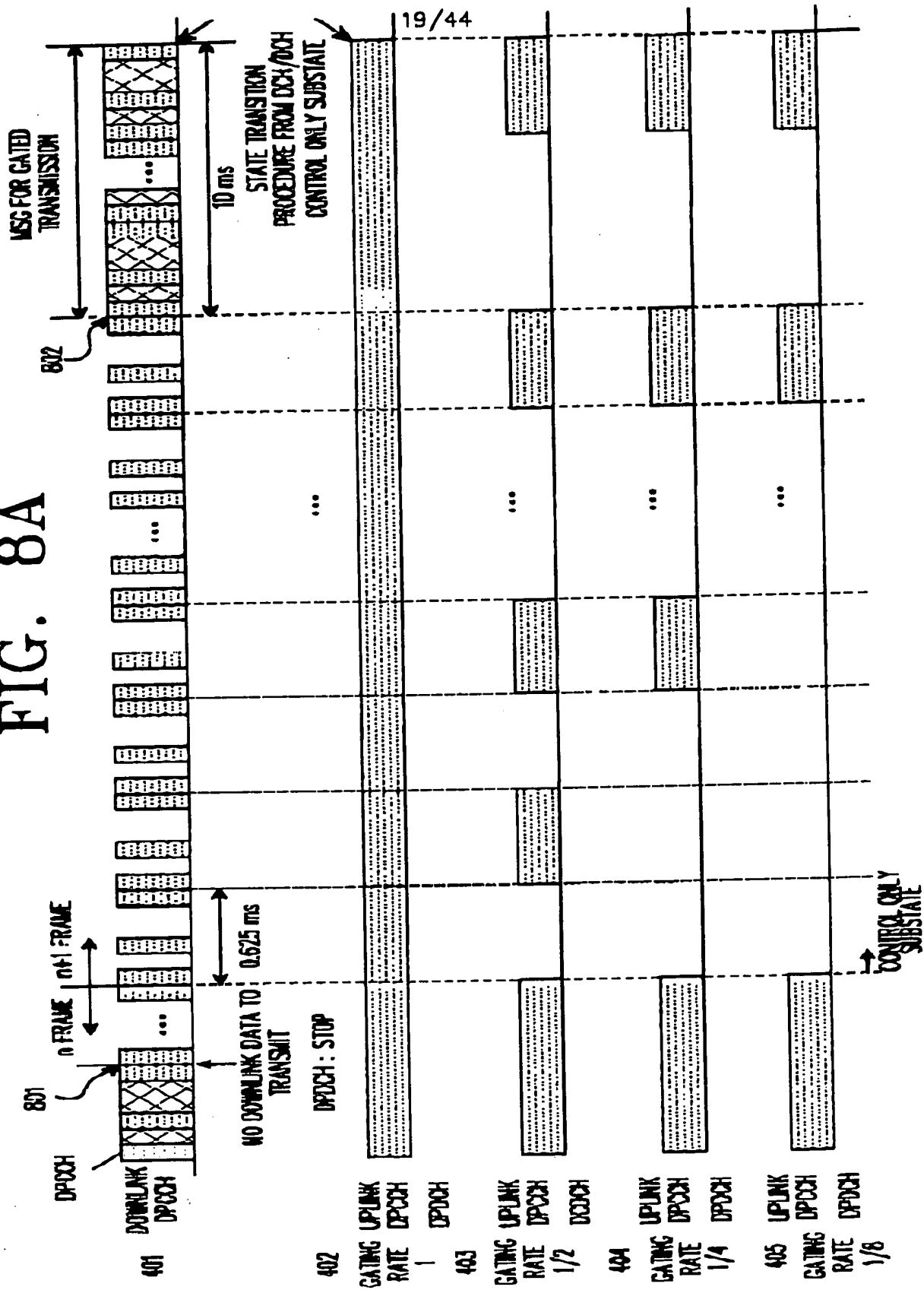


FIG. 8A





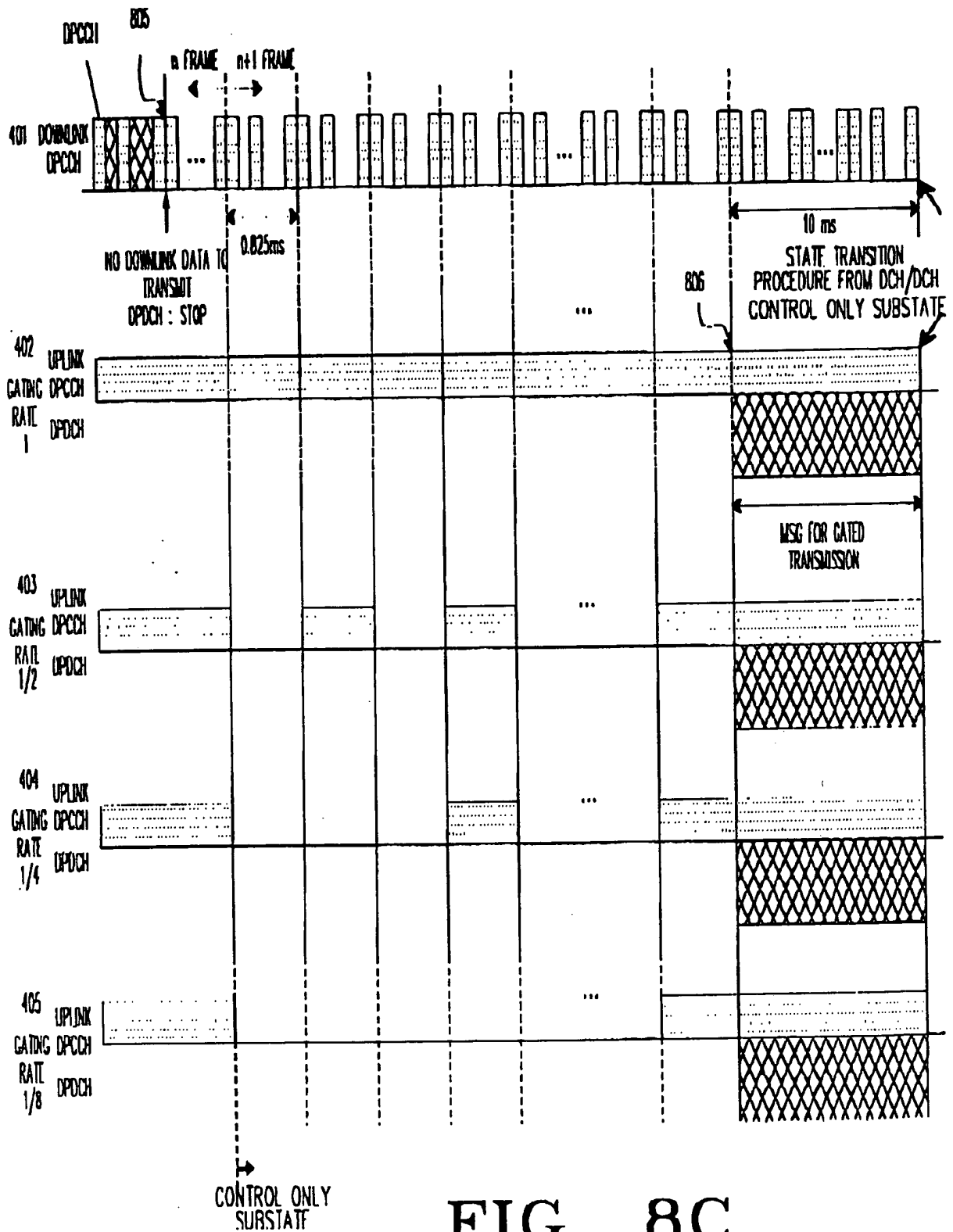


FIG. 8C

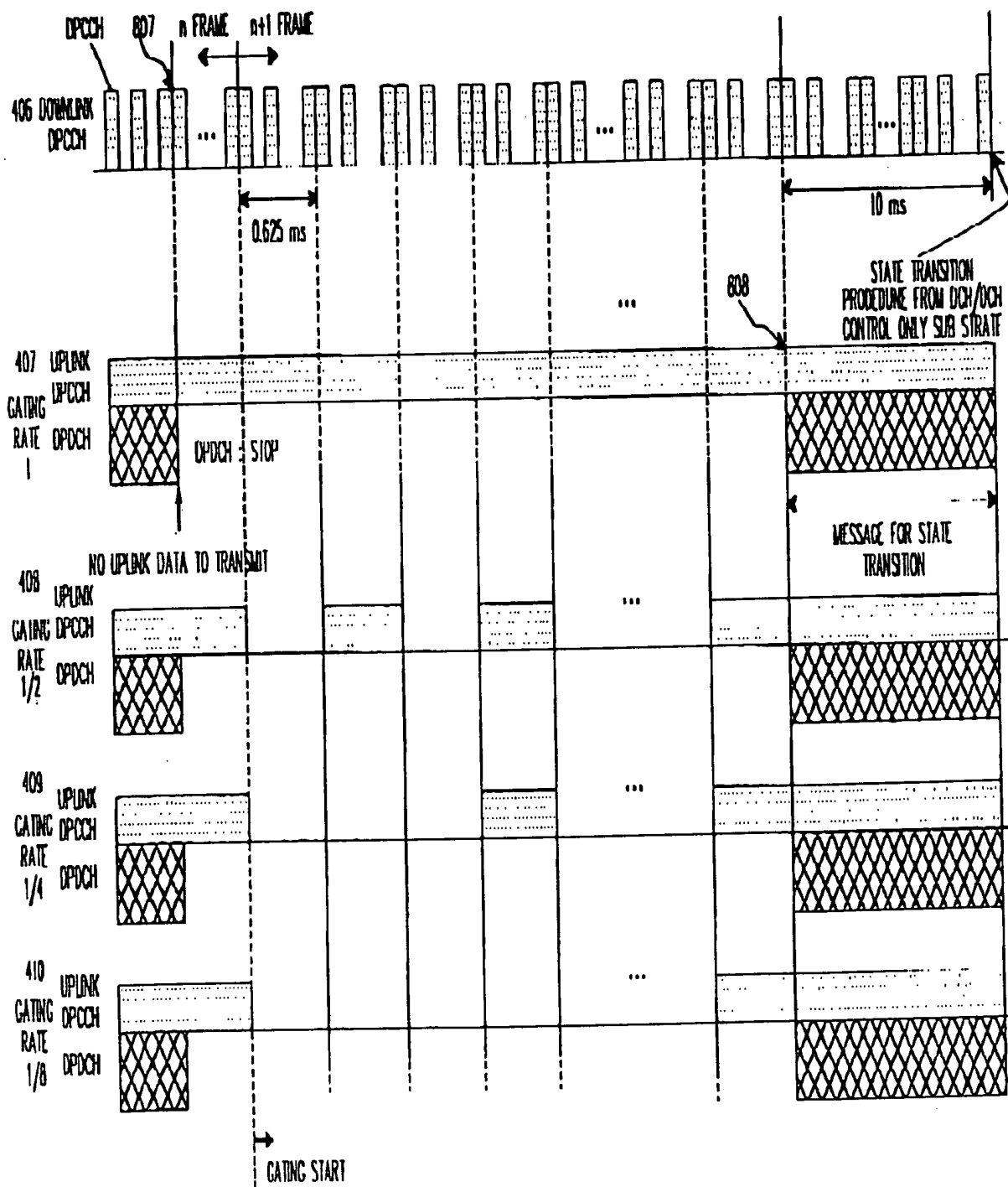


FIG. 8D

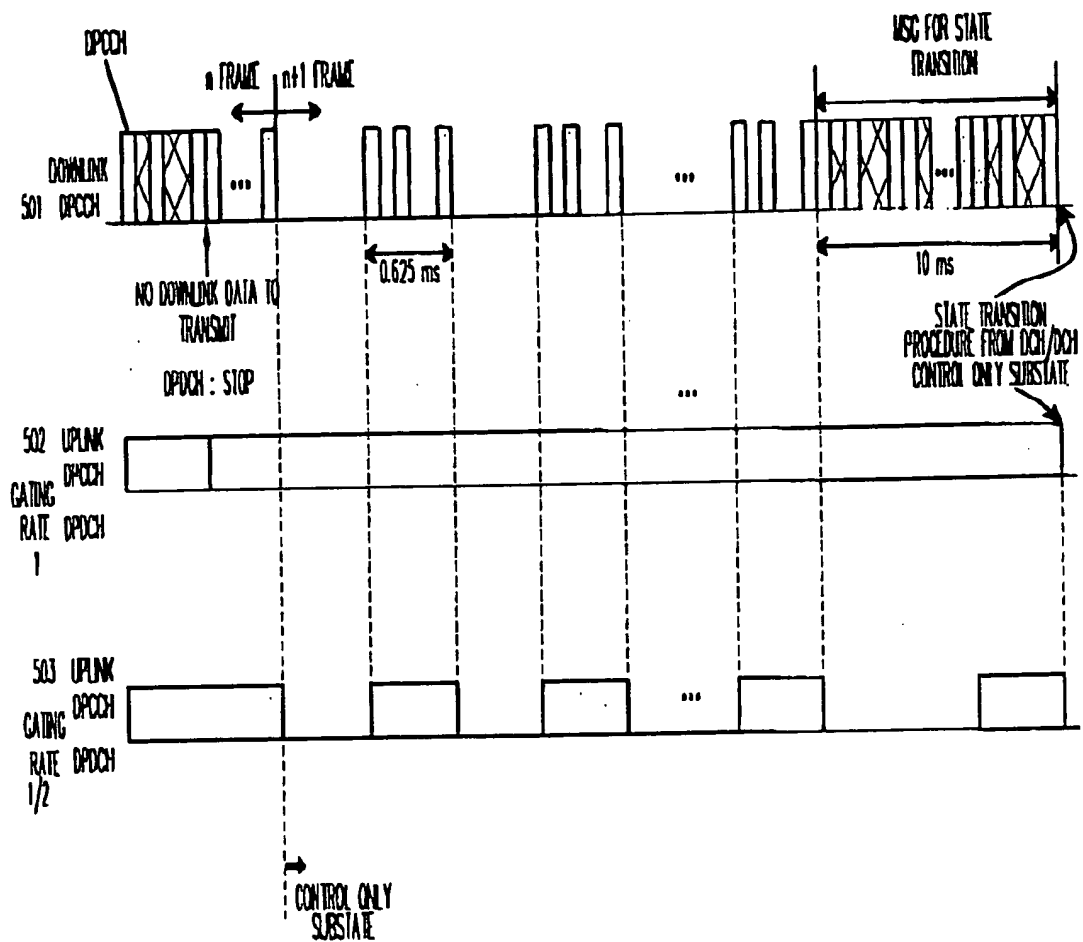


FIG. 9A

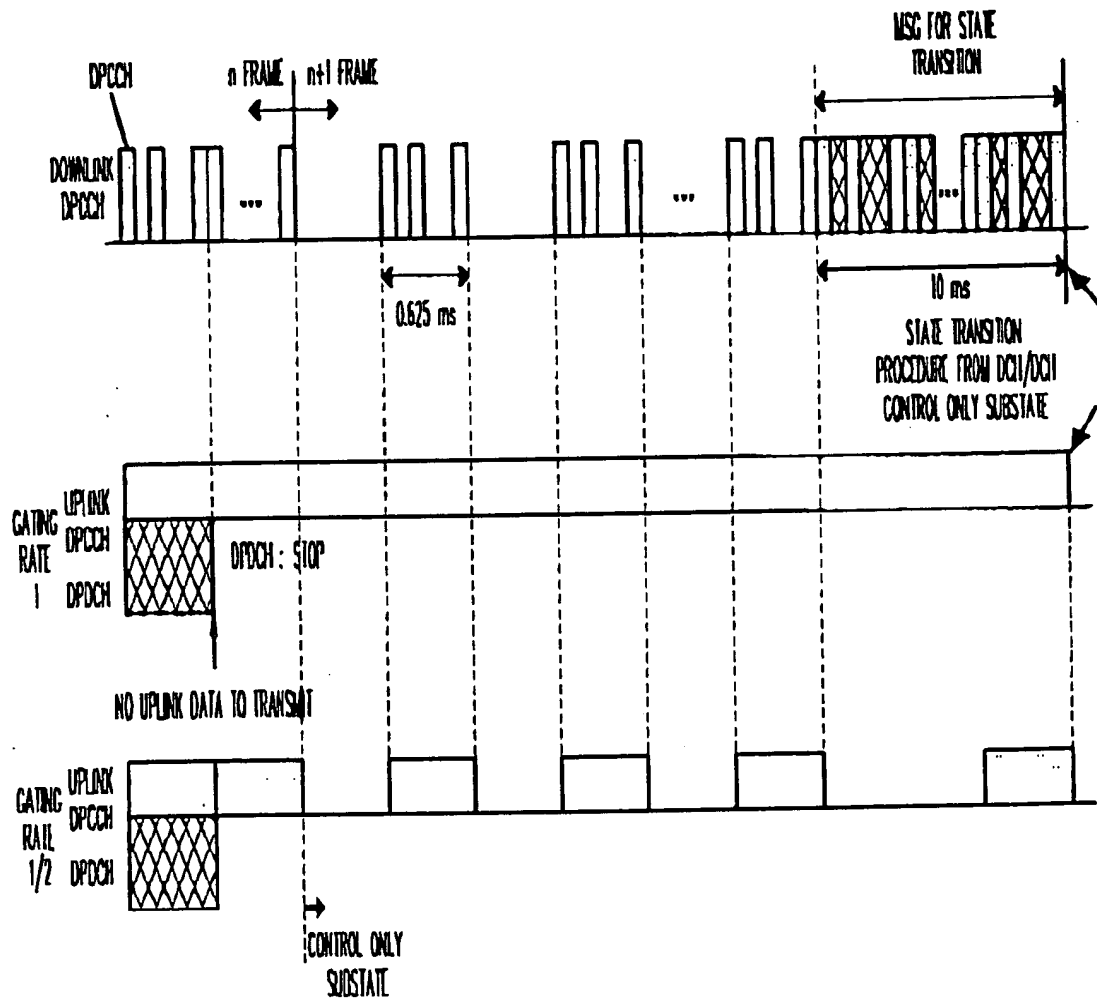


FIG. 9B



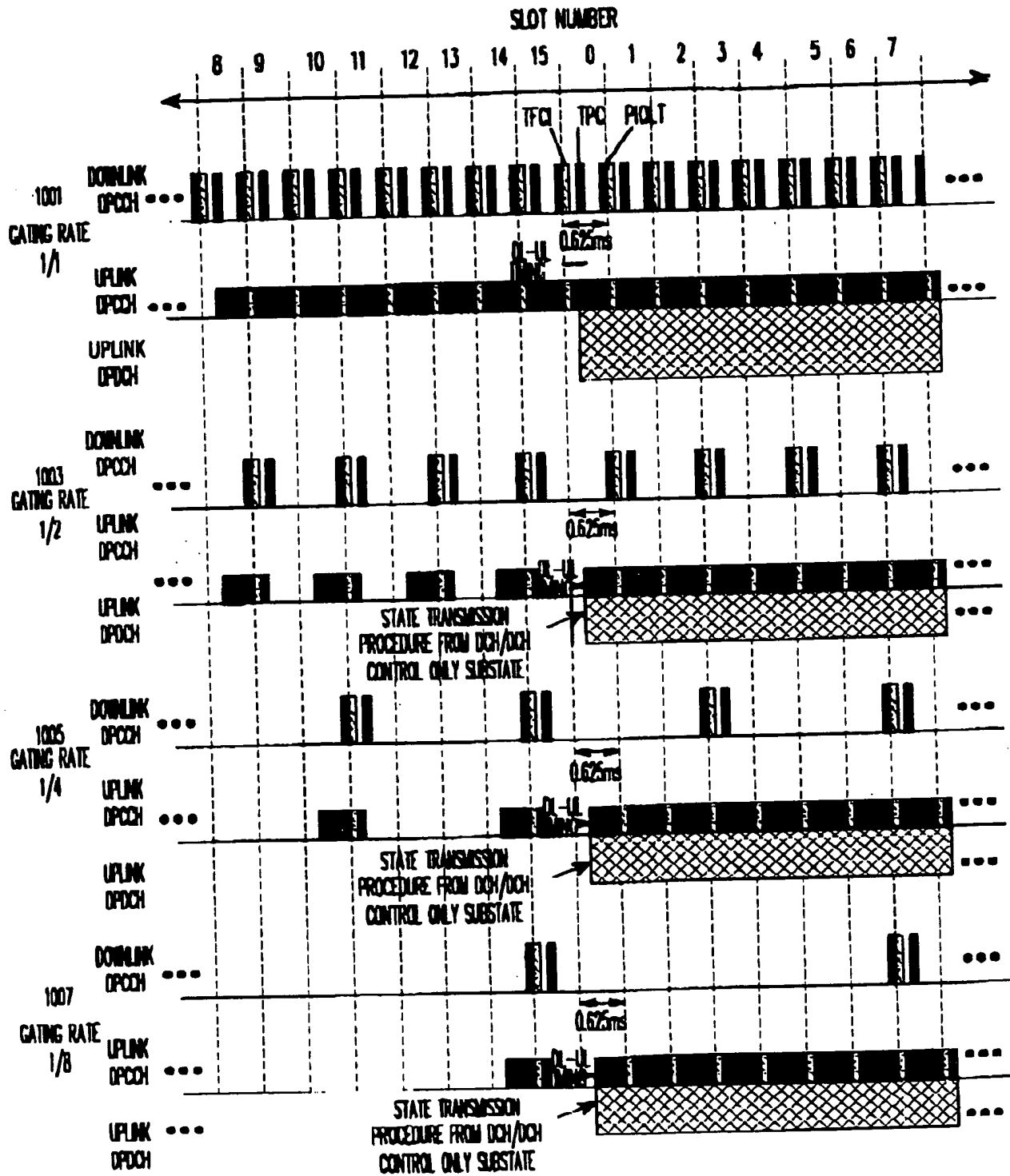


FIG. 10A

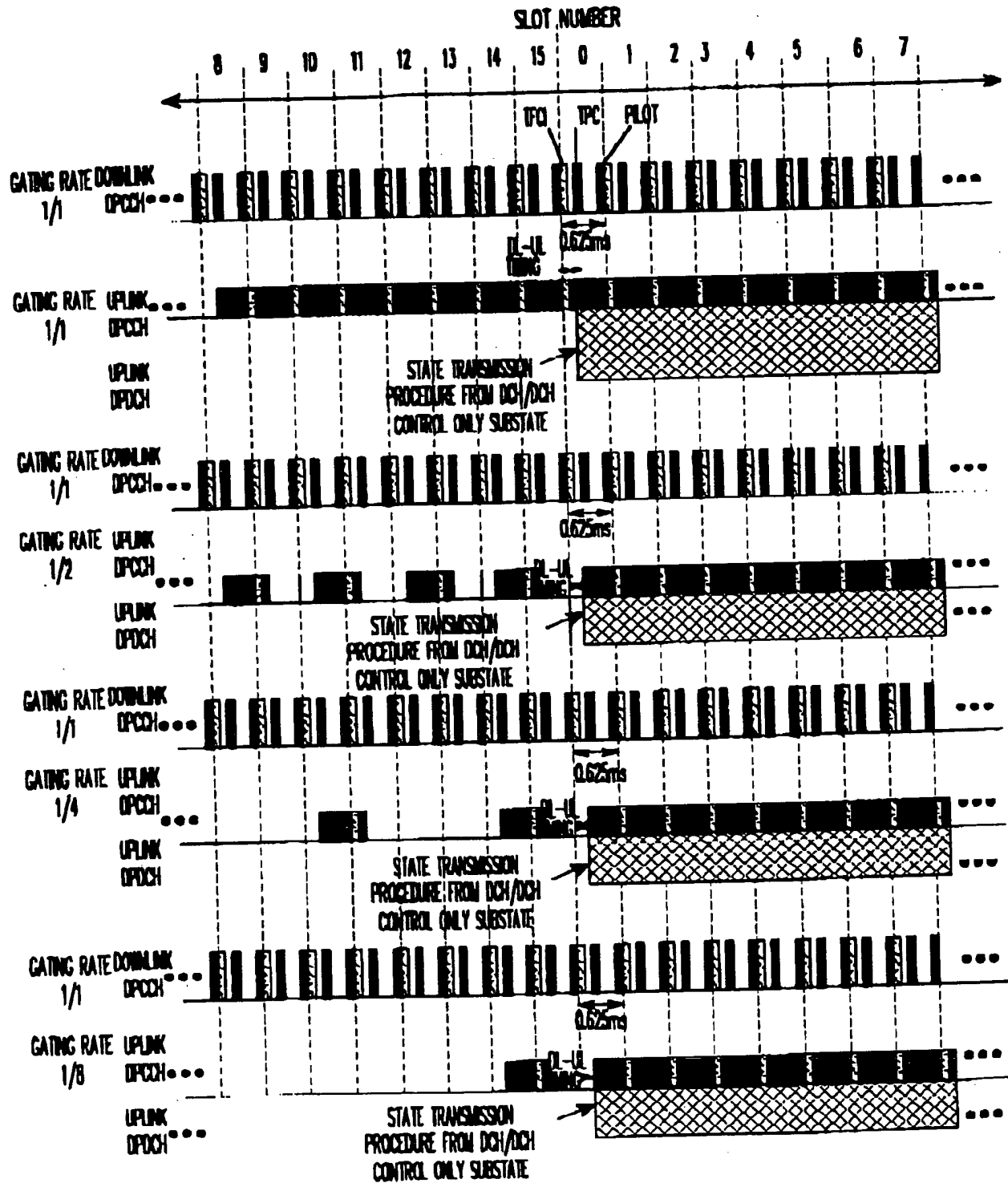


FIG. 10B

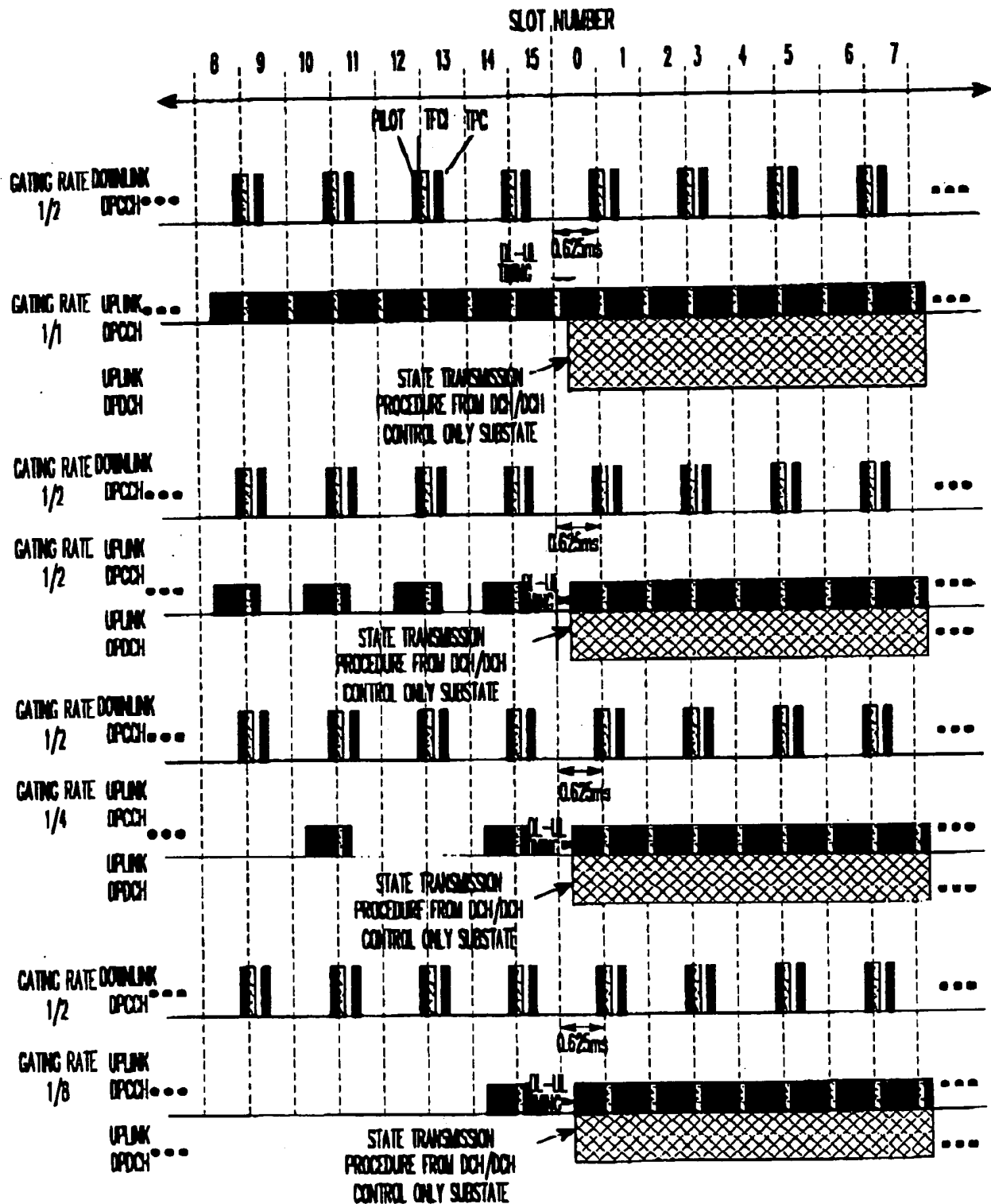


FIG. 10C

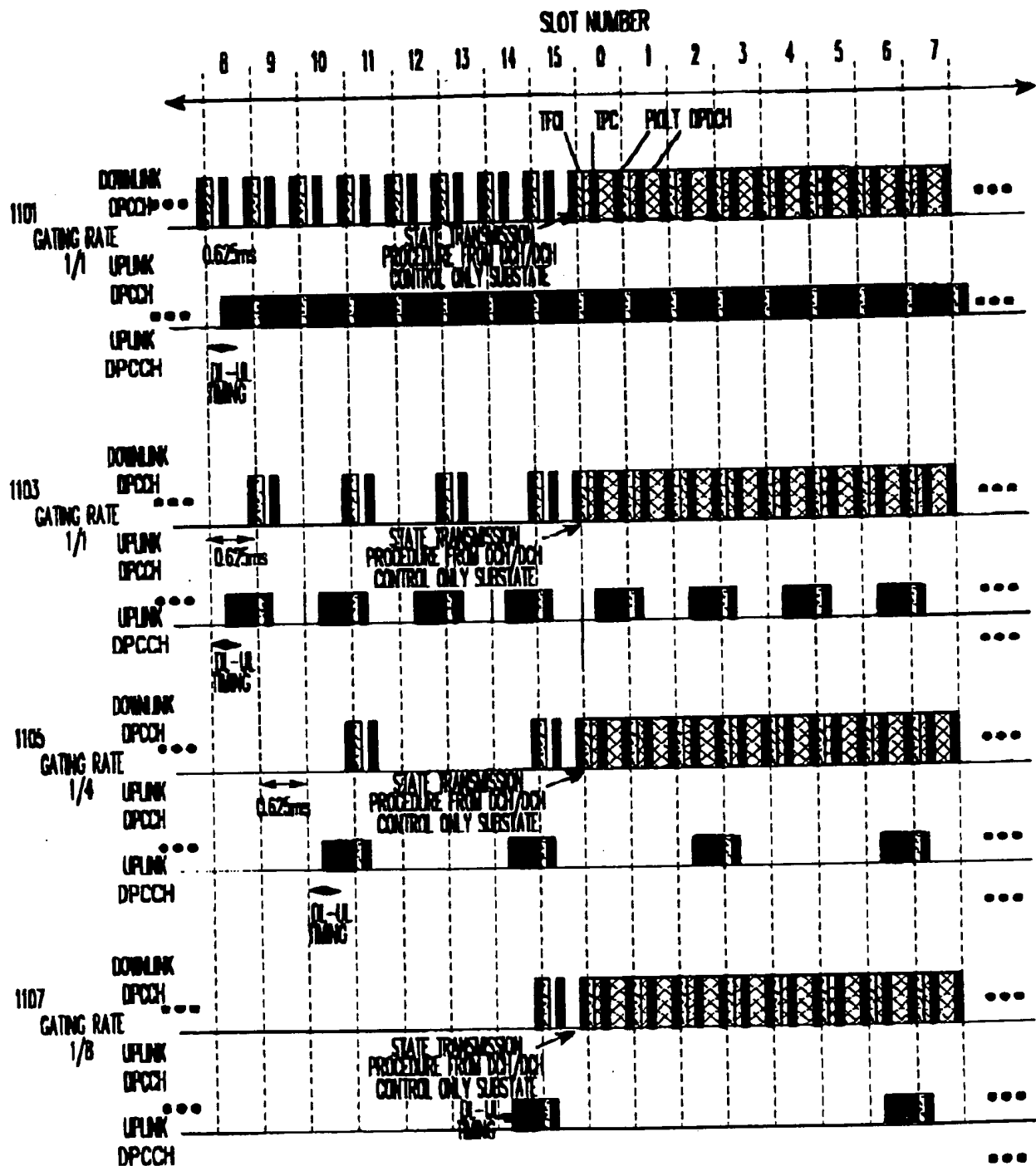


FIG. 11A

SLOT NUMBER

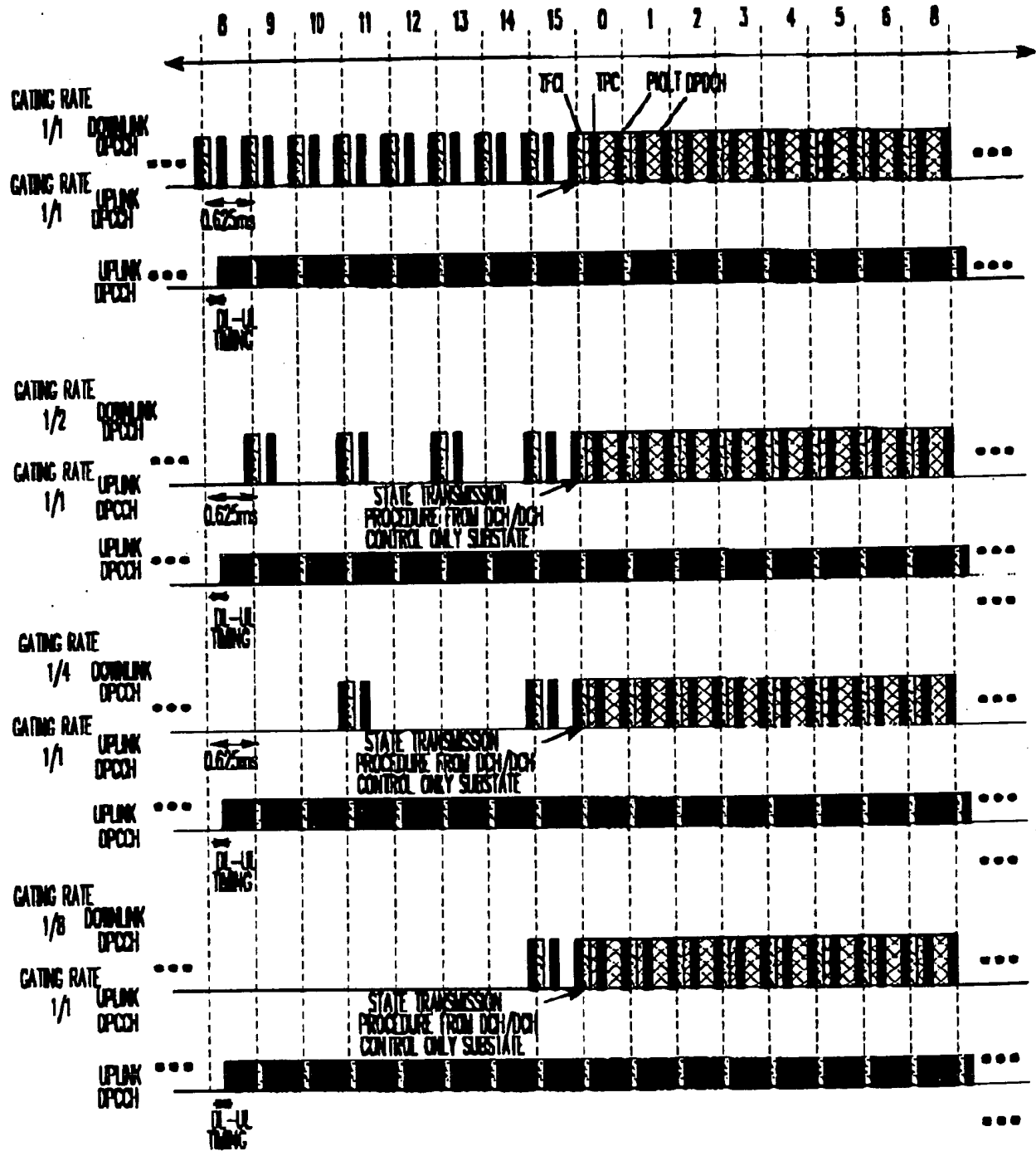


FIG. 11B

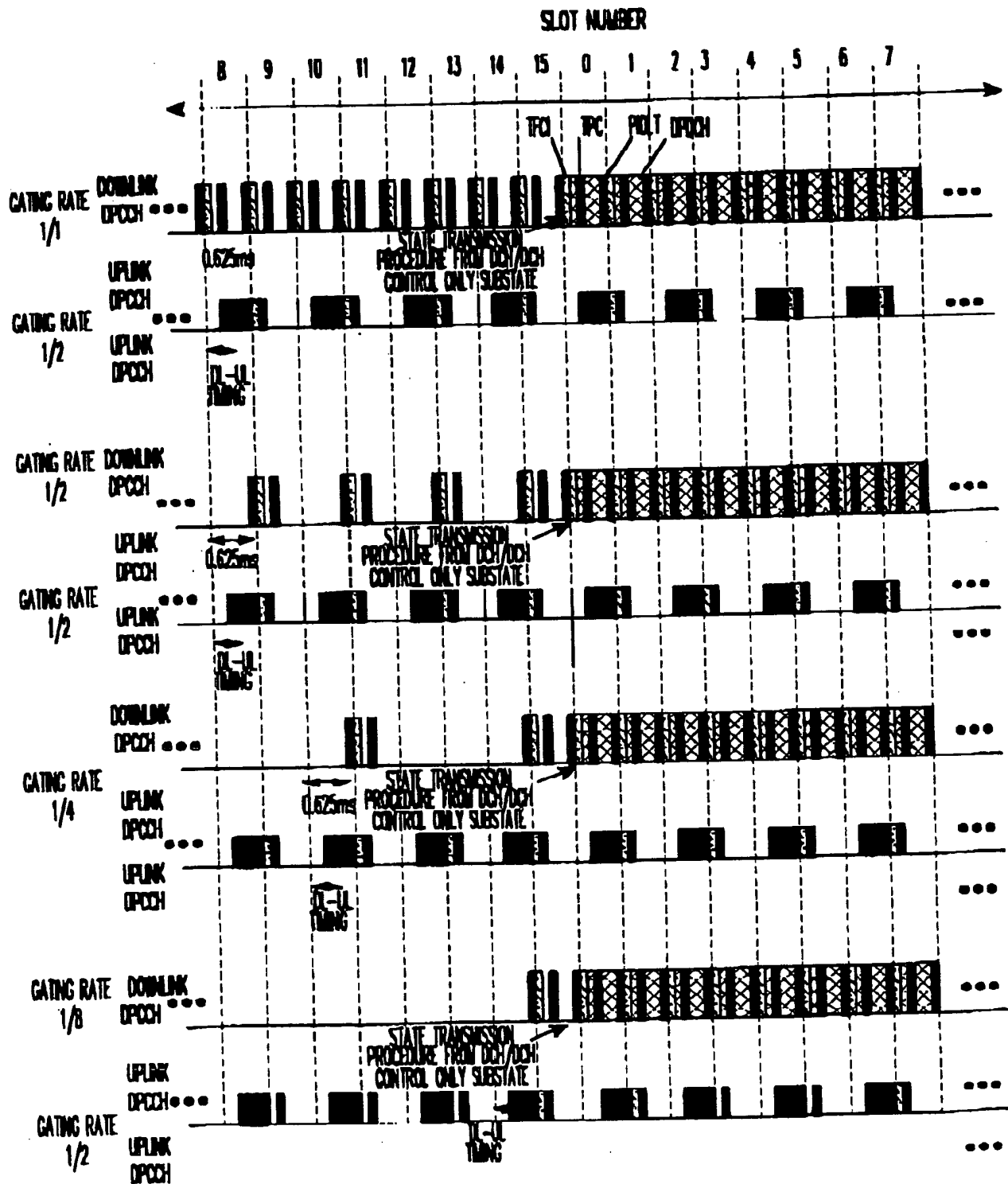


FIG. 11C

·31/44

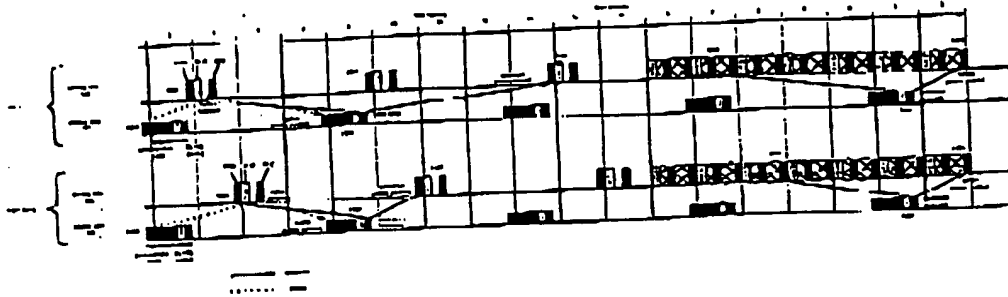


FIG 12A

[illegible]





FIG. 13A

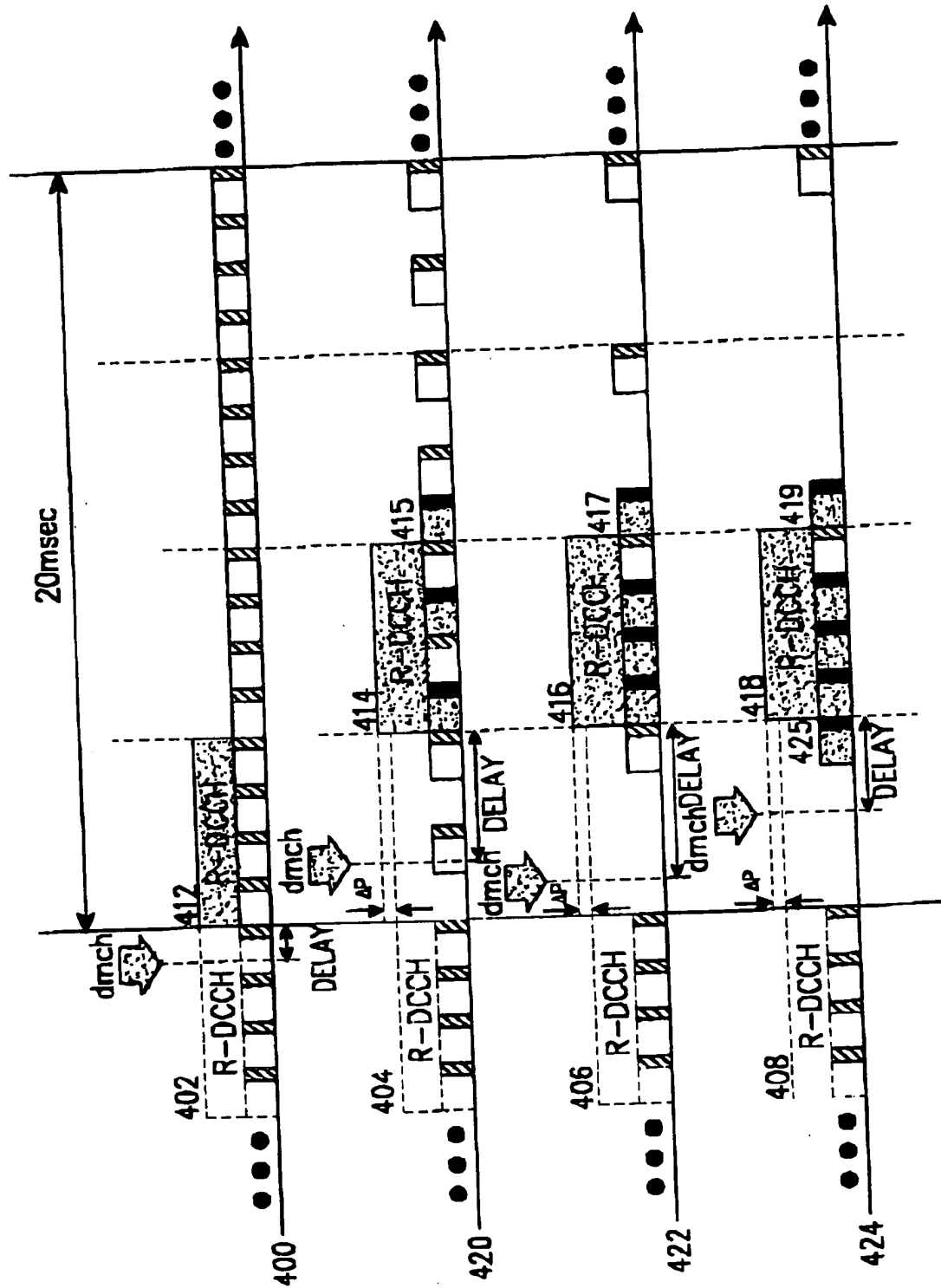


FIG. 13B

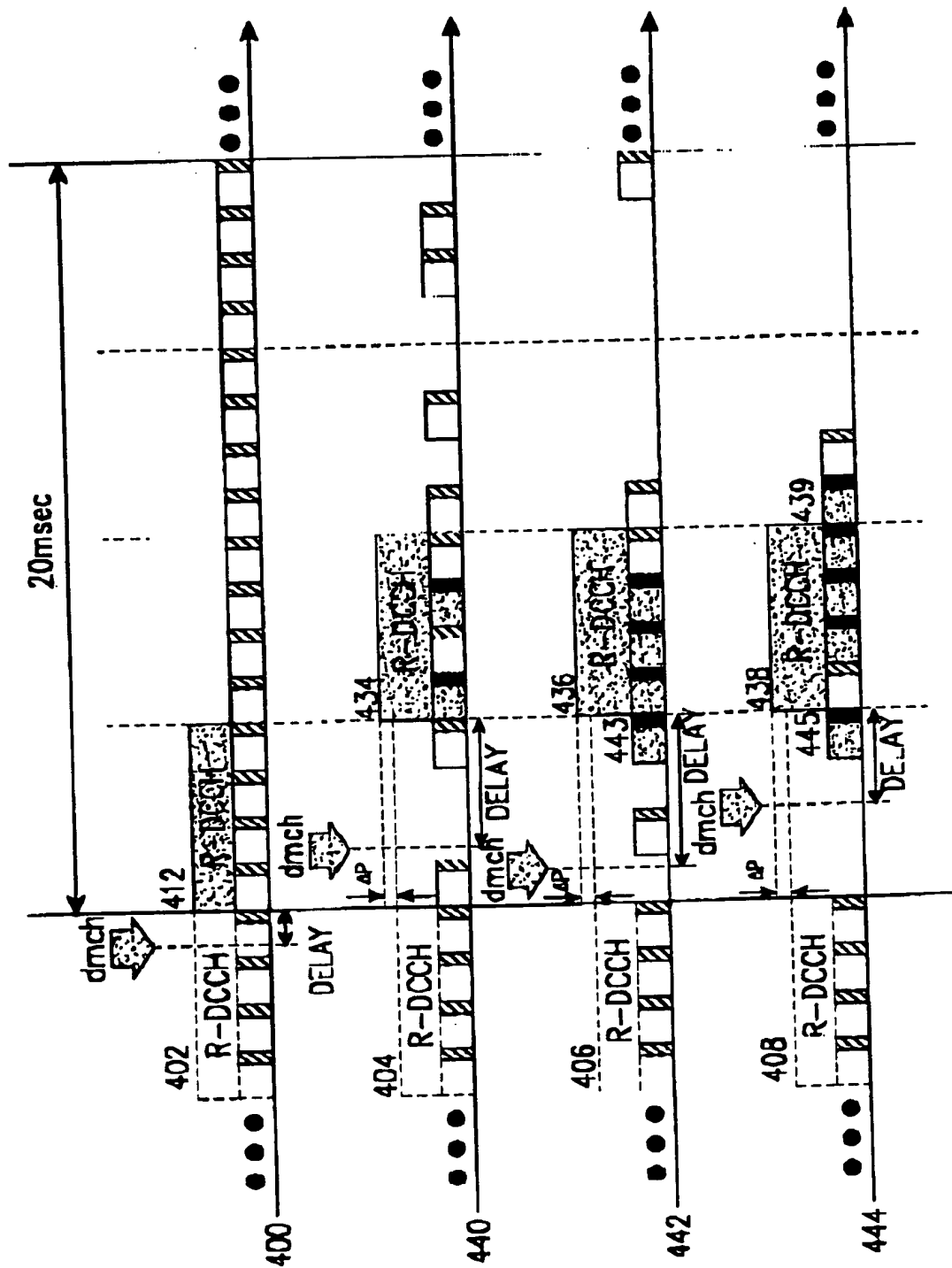


FIG. 13C

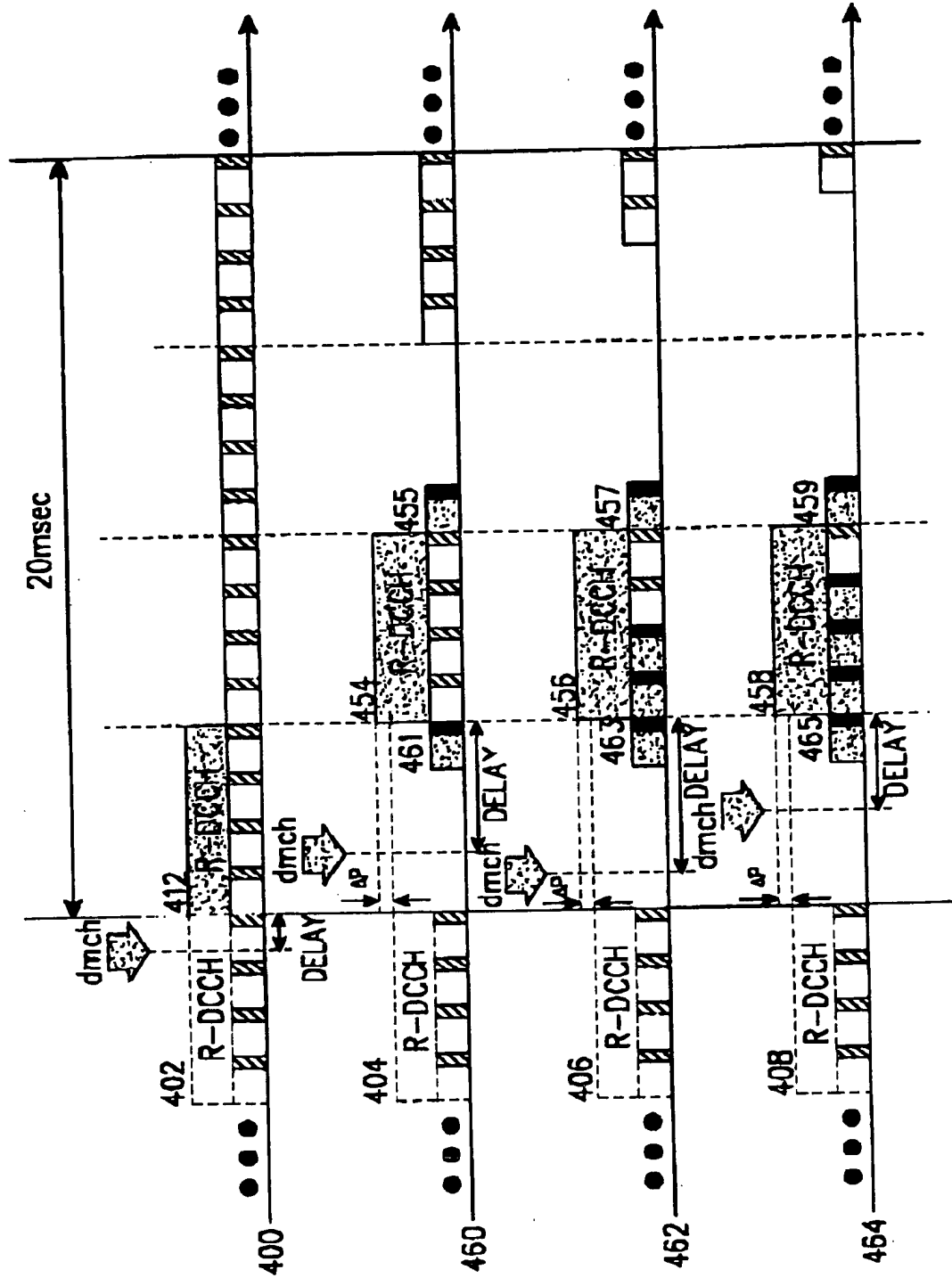
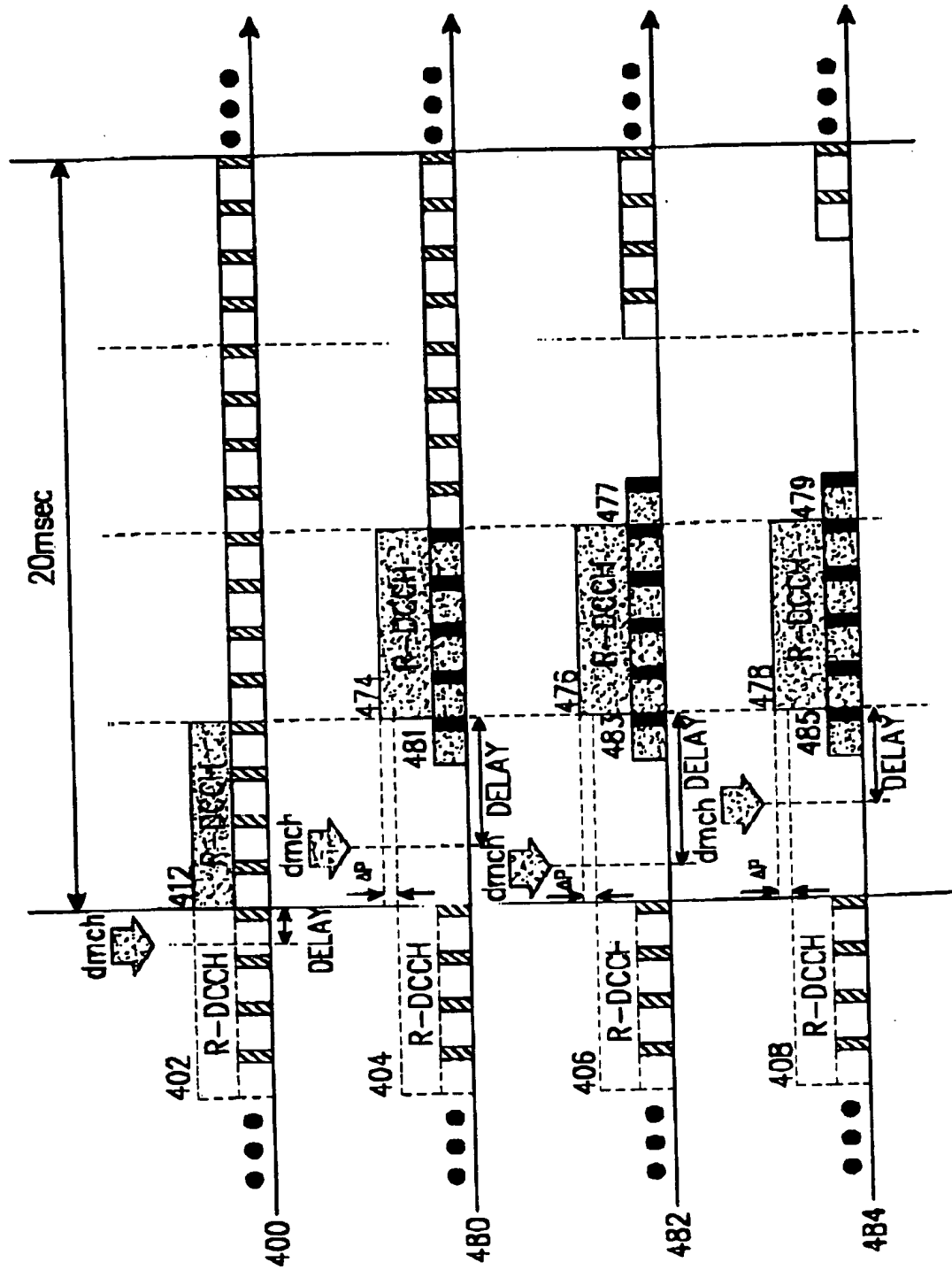


FIG. 13D



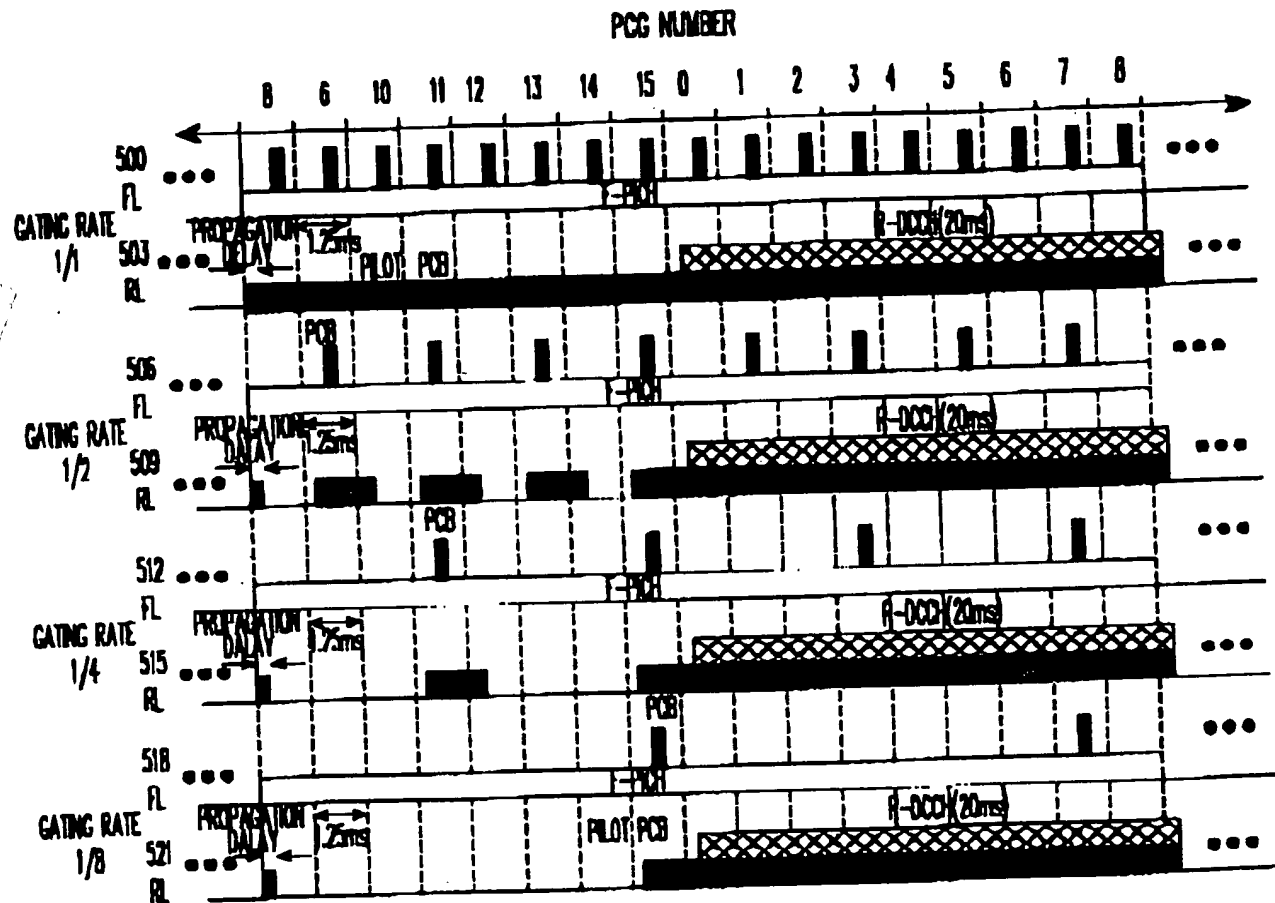


FIG. 14A

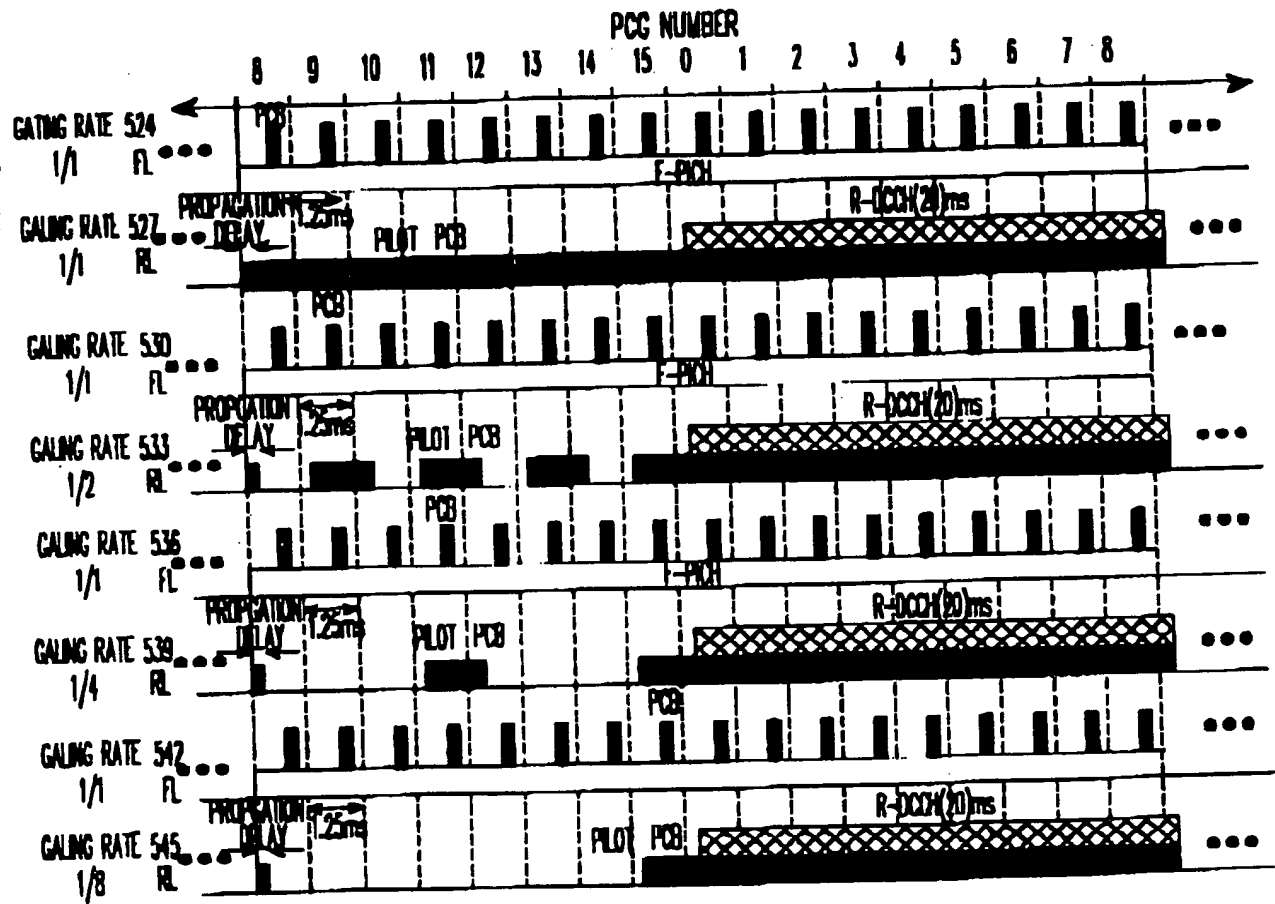


FIG. 14B

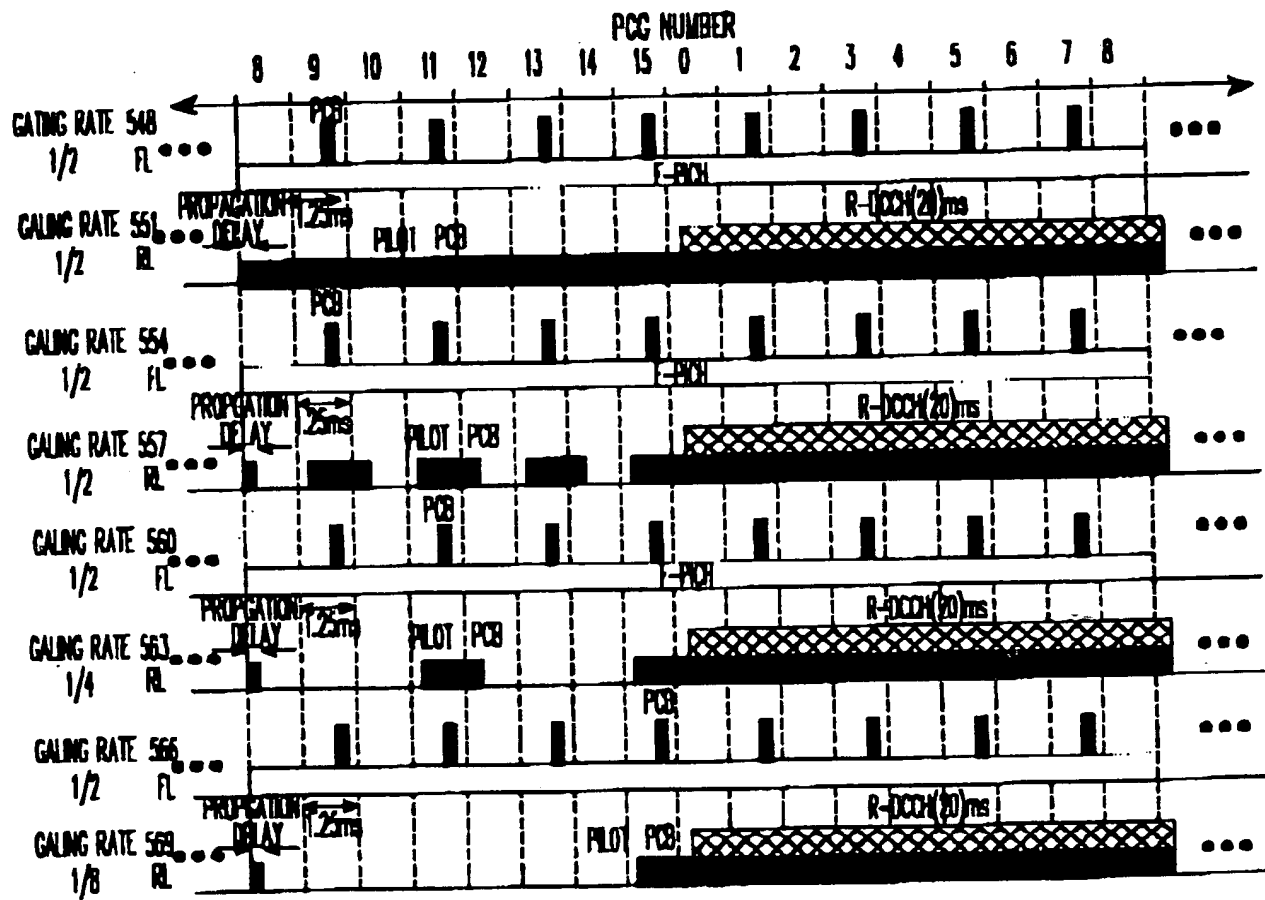


FIG. 14C

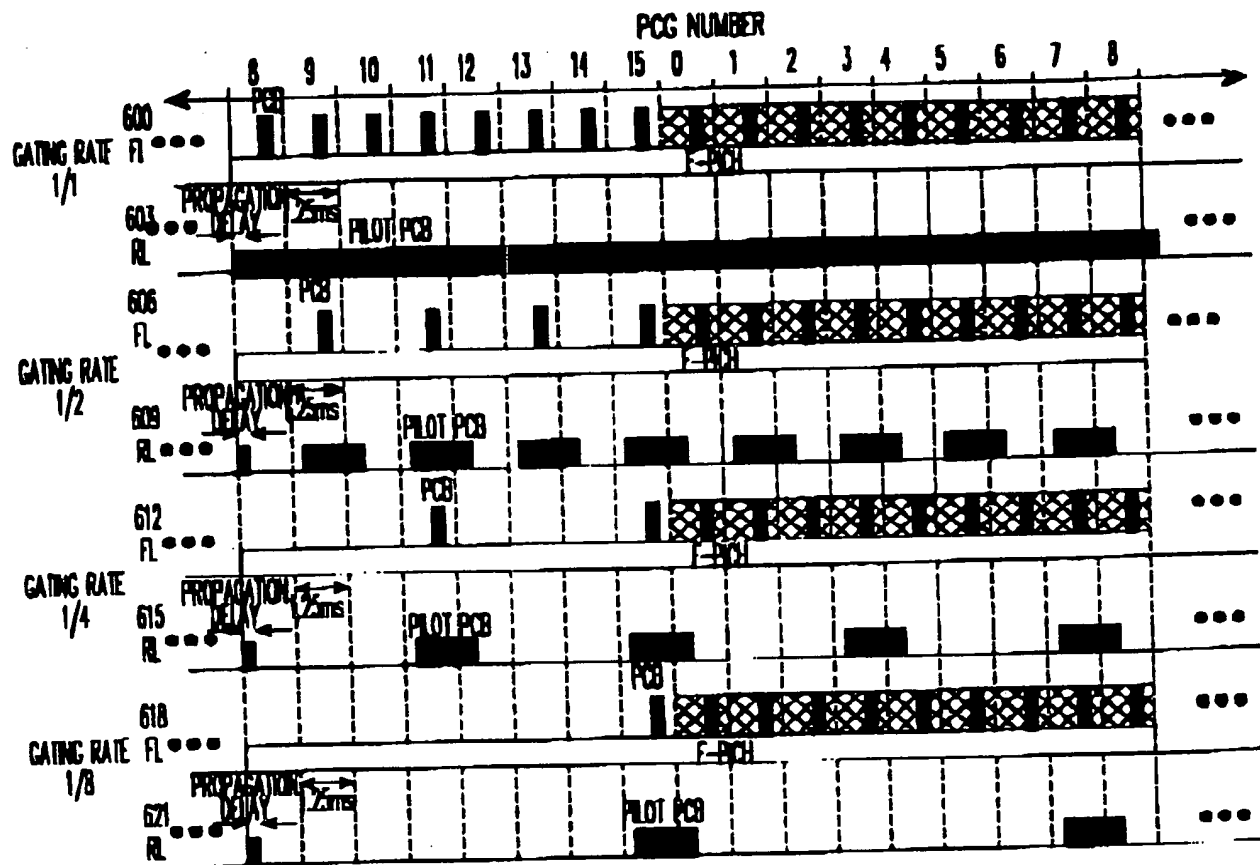


FIG. 15A



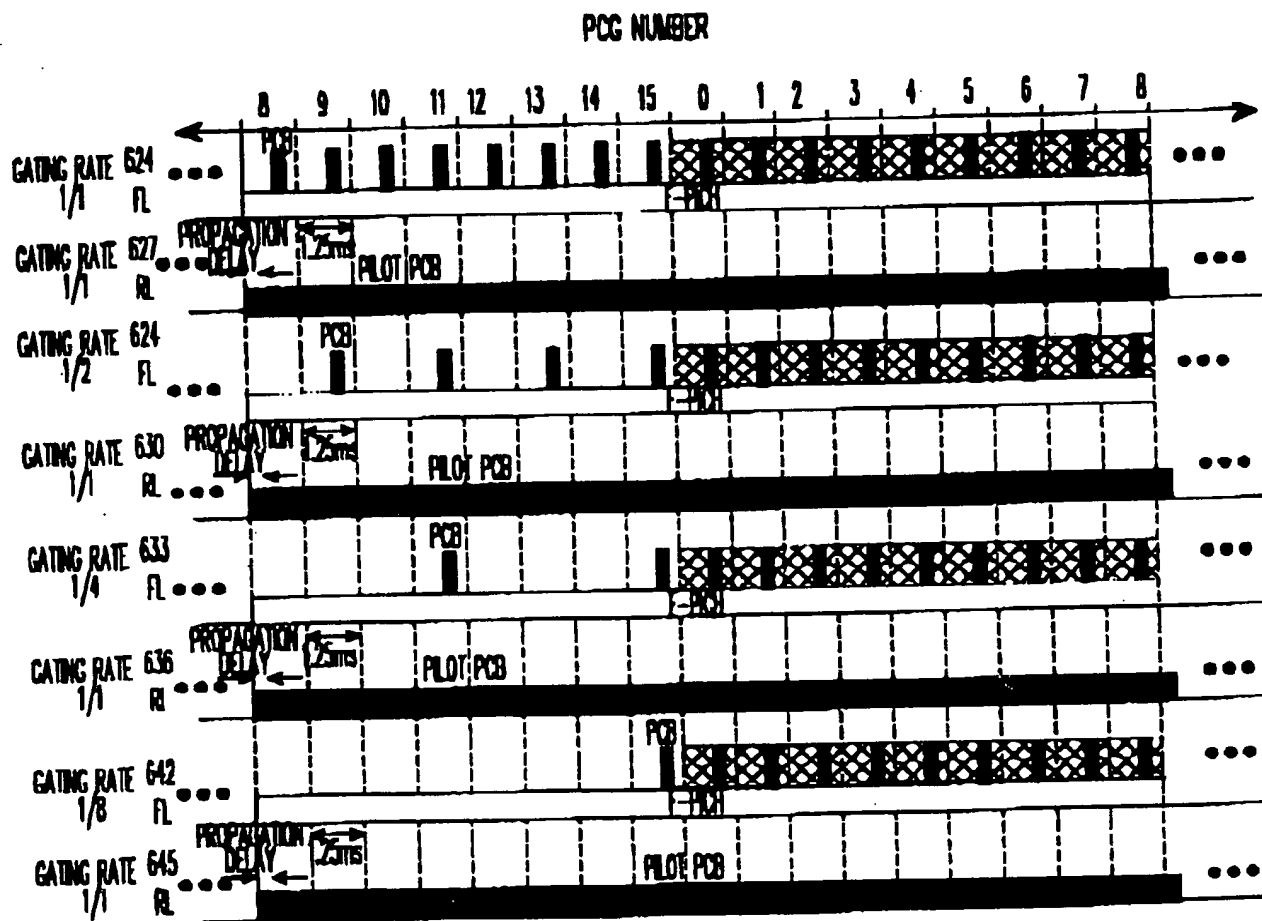


FIG. 15B

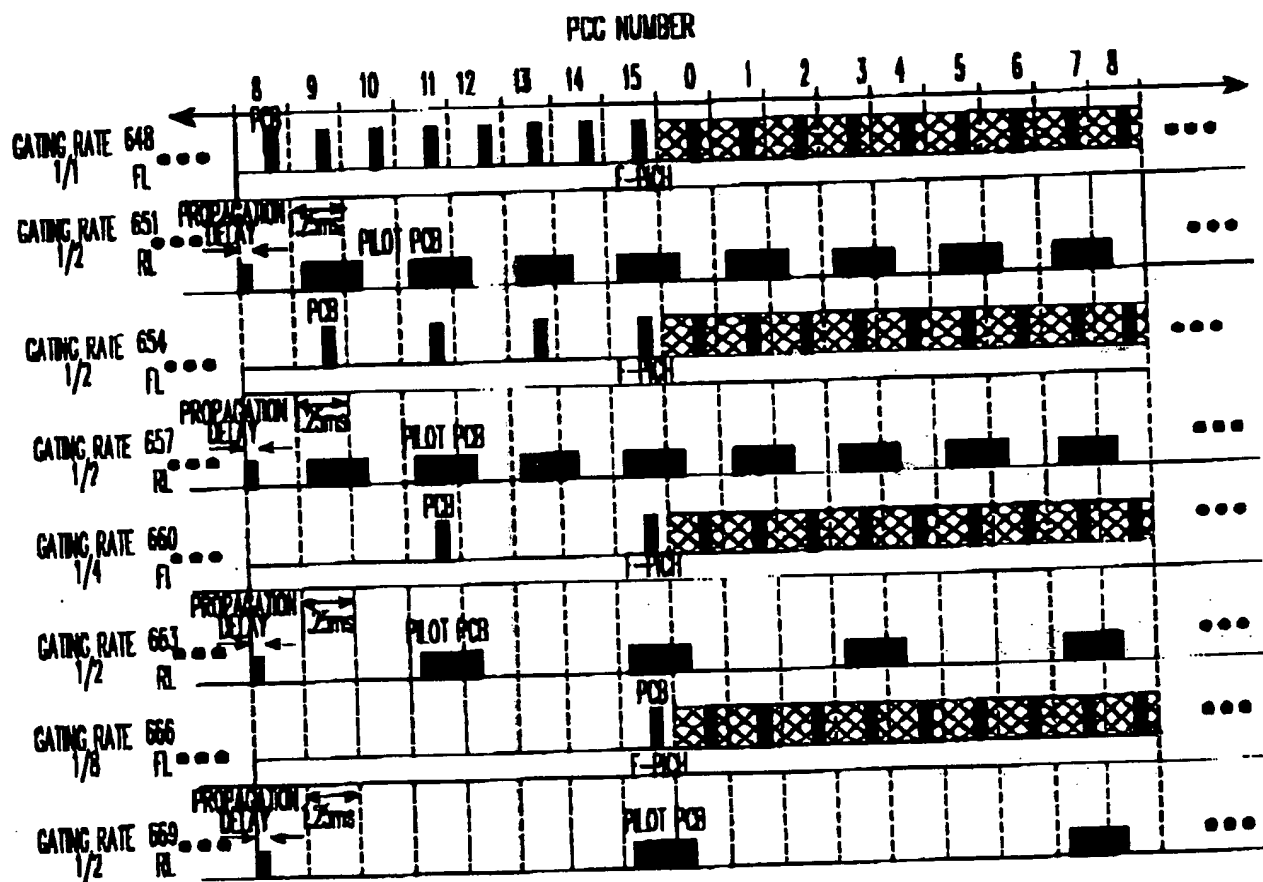


FIG. 15C

FIG. 16A

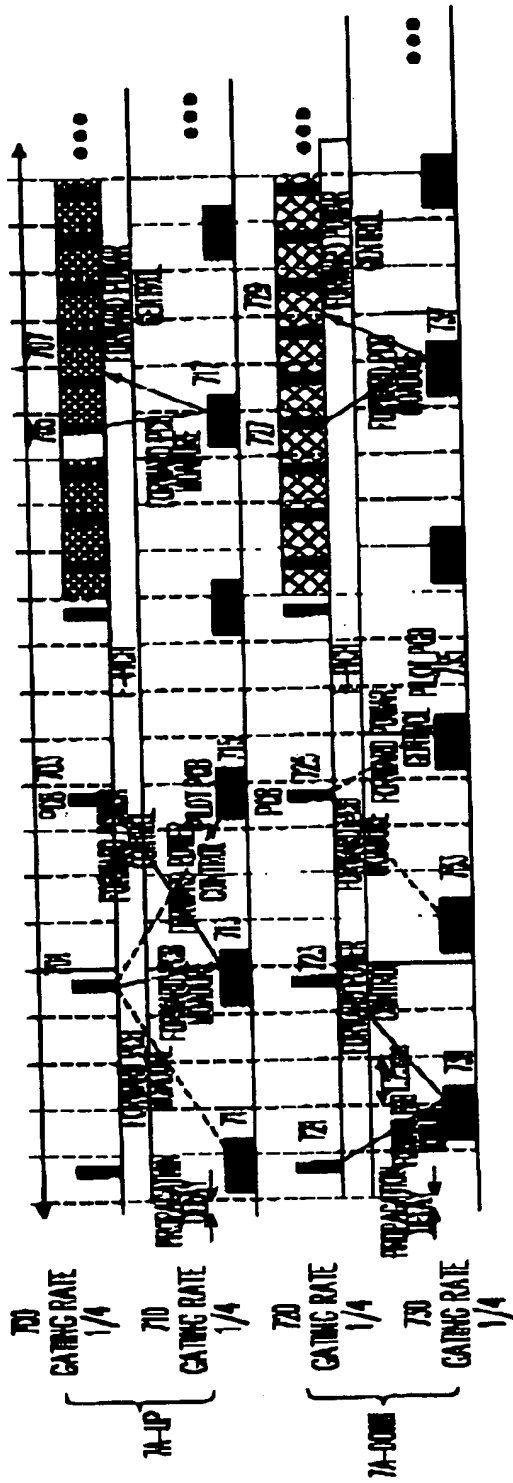


FIG. 16B

